

Digital Logic & Microprocessor Design with **Interfacing** 2nd Edition

Enoch O. Hwang



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Walter A. Triebel, Avtar Singh



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efficient manner Multi Core Embedded Systems presents a variety of perspectives that elucidate the technical challenges associated with such increased integration of homogeneous processors and heterogeneous multiple cores It offers an analysis that industry engineers and professionals will need to understand the physical details of both software and hardware in embedded architectures as well as their limitations and potential for future growth Discusses the available programming models spread across different abstraction levels The book begins with an overview of the evolution of multiprocessor architectures for embedded applications and discusses techniques for autonomous power management of system level parameters It addresses the use of existing open source and free tools originating from several application domains such as traffic modeling graph theory parallel computing and network simulation In addition the authors cover other important topics associated with multi core embedded systems such as Architectures and interconnects Embedded design methodologies Mapping of applications

A Practitioner's Guide to RISC Microprocessor Architecture Patrick H. Stakem,1996-04-25

Reduced Instruction Set Computers RISC reduce the number of instructions performed by the microprocessor This volume provides an overview of RISC as both a design philosophy and a marketing and technical force It introduces the fundamentals of RISC mic

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Computer Architecture William Gilreath,2023-01-08

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Tools for Teaching Computer Networking and Hardware Concepts Sarkar, Nurul,2006-02-28 This book offers concepts of the teaching and learning of computer networking and hardware by offering fundamental theoretical concepts illustrated with the use of interactive practical exercises Provided by publisher

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