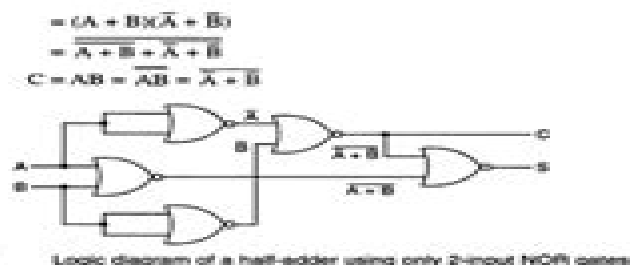


$$S = A\bar{B} + \bar{A}B = A\bar{B} + A\bar{A} + \bar{A}B + B\bar{B} \\ = A(\bar{A} + \bar{B}) + B(\bar{A} + \bar{B})$$



### The Full Adder:

A Full-adder is a combinational circuit that adds two bits and a carry and, outputs a sum bit and a carry bit. To add two binary numbers, each having two or more bits, the LSBs can be added by using a half-adder. The carry resulted from the addition of the LSBs is carried over to the next significant column and added to the two bits in that column. So, in the second and higher columns, the two data bits of that column and the carry bit generated from the addition in the previous column need to be added.

The full-adder adds the bits A and B and the carry from the previous column called the carry-in  $C_{in}$  and outputs the sum bit S and the carry bit called the carry-out  $C_{out}$ . The variable S gives the value of the least significant bit of the sum. The variable  $C_{out}$  gives the output carry. The eight rows under the input variables designate all possible combinations of 1s and 0s that these variables may have. The 1s and 0s for the output variables are determined from the arithmetic sum of the input bits. When all the bits are 0s, the output is 0. The S output is equal to 1 when only 1 input is equal to 1 or when all the inputs are equal to 1. The  $C_{out}$  has a carry of 1 if two or three inputs are equal to 1. (i.e) Number of inputs=2(A,B,C) and Number of outputs=2(sum and carry)

Inputs			Sum	Carry
A	B	$C_{in}$	S	$C_{out}$
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

(a) Truth table



(b) Block diagram

Full-adder.

From the truth table, a circuit that will produce the correct sum and carry bits in response to every possible combination of A,B and  $C_{in}$  is described by

$$S = ABC_{in} + \bar{A}BC_{in} + A\bar{B}C_{in} + AB\bar{C}_{in} = C_{in}(A \oplus B + AB) + C_{out}(A \oplus B + AB)$$

$$C_{out} = ABC_{in} + \bar{A}BC_{in} + A\bar{B}C_{in} + AB\bar{C}_{in} = C_{in}(A \oplus B) + AB$$

and

# **Solution Manual Combinational Logic Design**

**William James Dally, R. Curtis Harting**



## **Solution Manual Combinational Logic Design:**

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