

# Xilinx HDL Coding Hints

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HDLs contain many complex constructs that are difficult to understand at first. Also, the methods and examples included in HDL manuals do not always apply to the design of FPGAs. If you currently use HDLs to design ASICs, your established coding style may unnecessarily increase the number of gates or CLB levels in FPGA designs.

HDL synthesis tools implement logic based on the coding style of your design. To learn how to efficiently code with HDLs, you can attend training classes, read reference and methodology notes, and refer to synthesis guidelines and templates available from Xilinx and the synthesis vendors. When coding your designs, remember that HDLs are mainly hardware description languages. You should try to find a balance between the quality of the end hardware results and the speed of simulation.

The coding hints and examples included in this chapter are not intended to teach you every aspect of VHDL or Verilog, but they should help you develop an efficient coding style.

The following sections are included in this chapter.

- “Comparing Synthesis and Simulation Results”
- “Selecting HDL Formatting Styles”
- “Using Schematic Design Hints with HDL Designs”

## Comparing Synthesis and Simulation Results

VHDL and Verilog are hardware description and simulation languages that were not originally intended as input to synthesis. Therefore, many hardware description and simulation constructs are not supported by synthesis tools. In addition, the various synthesis tools use different subsets of VHDL and Verilog. VHDL and Verilog semantics are well defined for design simulation. The synthesis tools must adhere to these semantics to ensure that designs simulate the same way before and after synthesis. Follow the guidelines presented below to create code that simulates the same way before and after synthesis.

# Xilinx Coding Style Guidelines

**Pong P. Chu**



## **Xilinx Coding Style Guidelines:**

Designing with Xilinx® FPGAs Sanjay Churiwala, 2016-10-20 This book helps readers to implement their designs on Xilinx FPGAs. The authors demonstrate how to get the greatest impact from using the Vivado Design Suite which delivers a SoC strength IP centric and system centric next generation development environment that has been built from the ground up to address the productivity bottlenecks in system level integration and implementation. This book is a hands on guide for both users who are new to FPGA designs as well as those currently using the legacy Xilinx tool set ISE but are now moving to Vivado. Throughout the presentation the authors focus on key concepts major mechanisms for design entry and methods to realize the most efficient implementation of the target design with the least number of iterations. **Effective Coding with VHDL** Ricardo Jasinski, 2016-05-27 A guide to applying software design principles and coding practices to VHDL to improve the readability maintainability and quality of VHDL code. This book addresses an often neglected aspect of the creation of VHDL designs. A VHDL description is also source code and VHDL designers can use the best practices of software development to write high quality code and to organize it in a design. This book presents this unique set of skills teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware. The concepts introduced here will help readers write code that is easier to understand and more likely to be correct with improved readability maintainability and overall quality. After a brief review of VHDL the book presents fundamental design principles for writing code discussing such topics as design quality architecture modularity abstraction and hierarchy. Building on these concepts the book then introduces and provides recommendations for each basic element of VHDL code including statements design units types data objects and subprograms. The book covers naming data objects and functions commenting the source code and visually presenting the code on the screen. All recommendations are supported by detailed rationales. Finally the book explores two uses of VHDL synthesis and testbenches. It examines the key characteristics of code intended for synthesis distinguishing it from code meant for simulation and then demonstrates the design and implementation of testbenches with a series of examples that verify different kinds of models including combinational sequential and FSM code. Examples from the book are also available on a companion website enabling the reader to experiment with the complete source code. Rapid System Prototyping with FPGAs R. C. Cofer, Benjamin F. Harding, 2011-03-31 The push to move products to market as quickly and cheaply as possible is fiercer than ever and accordingly engineers are always looking for new ways to provide their companies with the edge over the competition. Field Programmable Gate Arrays FPGAs which are faster denser and more cost effective than traditional programmable logic devices PLDs are quickly becoming one of the most widespread tools that embedded engineers can utilize in order to gain that needed edge. FPGAs are especially popular for prototyping designs due to their superior speed and efficiency. This book hones in on that rapid prototyping aspect of FPGA use showing designers exactly how they can cut time off production cycles.

and save their companies money drained by costly mistakes via prototyping designs with FPGAs first Reading it will take a designer with a basic knowledge of implementing FPGAs to the next level of FPGA use because unlike broad beginner books on FPGAs this book presents the required design skills in a focused practical example oriented manner In the trenches expert authors assure the most applicable advice to practicing engineers Dual focus on successfully making critical decisions and avoiding common pitfalls appeals to engineers pressured for speed and perfection Hardware and software are both covered in order to address the growing trend toward cross pollination of engineering expertise **Hardware/Software**

**Architectures for Low-Power Embedded Multimedia Systems** Muhammad Shafique, Jörg Henkel, 2011-07-25 This book presents techniques for energy reduction in adaptive embedded multimedia systems based on dynamically reconfigurable processors The approach described will enable designers to meet performance area constraints while minimizing video quality degradation under various run time scenarios Emphasis is placed on implementing power energy reduction at various abstraction levels To enable this novel techniques for adaptive energy management at both processor architecture and application architecture levels are presented such that both hardware and software adapt together minimizing overall energy consumption under unpredictable design compile time scenarios **Digital Design (VHDL)** Peter J. Ashenden, 2007-10-24

Digital Design An Embedded Systems Approach Using VHDL provides a foundation in digital design for students in computer engineering electrical engineering and computer science courses It takes an up to date and modern approach of presenting digital logic design as an activity in a larger systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized VHDL examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of VHDL examples to demonstrate HDL hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments Includes worked examples throughout to enhance the reader s understanding and retention of the material Companion Web site includes links to tools for FPGA design from Synplicity Mentor Graphics and Xilinx VHDL source code for all the examples in the book lecture slides laboratory projects and solutions to exercises **100 Power Tips for FPGA Designers , FPGA Prototyping by**

**SystemVerilog Examples** Pong P. Chu, 2018-05-30 A hands on introduction to FPGA prototyping and SoC design This is the successor edition of the popular FPGA Prototyping by Verilog Examples text It follows the same learning by doing approach to teach the fundamentals and practices of HDL synthesis and FPGA prototyping The new edition uses a coherent series of examples to demonstrate the process to develop sophisticated digital circuits and IP intellectual property cores integrate

them into an SoC system on a chip framework realize the system on an FPGA prototyping board and verify the hardware and software operation The examples start with simple gate level circuits progress gradually through the RT register transfer level modules and lead to a functional embedded system with custom I O peripherals and hardware accelerators Although it is an introductory text the examples are developed in a rigorous manner and the derivations follow the strict design guidelines and coding practices used for large complex digital systems The book is completely updated and uses the SystemVerilog language which absorbs the Verilog language It presents the hardware design in the SoC context and introduces the hardware software co design concept Instead of treating examples as isolated entities the book integrates them into a single coherent SoC platform that allows readers to explore both hardware and software programmability and develop complex and interesting embedded system projects The new edition Adds four general purpose IP cores which are multi channel PWM pulse width modulation controller I2C controller SPI controller and XADC Xilinx analog to digital converter controller Introduces a music synthesizer constructed with a DDFS direct digital frequency synthesis module and an ADSR attack decay sustain release envelope generator Expands the original video controller into a complete stream based video subsystem that incorporates a video synchronization circuit a test pattern generator an OSD on screen display controller a sprite generator and a frame buffer Provides a detailed discussion on blocking and nonblocking statements and coding styles Describes basic concepts of software hardware co design with Xilinx MicroBlaze MCS soft core processor Provides an overview of bus interconnect and interface circuit Presents basic embedded system software development Suggests additional modules and peripherals for interesting and challenging projects FPGA Prototyping by SystemVerilog Examples makes a natural companion text for introductory and advanced digital design courses and embedded system courses It also serves as an ideal self teaching guide for practicing engineers who wish to learn more about this emerging area of interest

**A Hands-On Guide to Designing Embedded Systems** Adam Taylor, Dan Binnun, Saket

Srivastava, 2021-10-31 This practical resource introduces readers to the design of field programmable gate array systems FPGAs Techniques and principles that can be applied by the engineer to understand challenges before starting a project are presented The book provides a framework from which to work and approach development of embedded systems that will give readers a better understanding of the issues at hand and can develop solution which presents lower technical and programmatic risk and a faster time to market Programmatic and system considerations are introduced providing an overview of the engineering life cycle when developing an electronic solution from concept to completion Hardware design architecture is discussed to help develop an architecture to meet the requirements placed upon it and the trade offs required to achieve the budget The FPGA development lifecycle and the inputs and outputs from each stage including design test benches synthesis mapping place and route and power estimation are also presented Finally the importance of reliability why it needs to be considered the current standards that exist and the impact of not considering this is explained Written by

experts in the field this is the first book by engineers in the trenches that presents FPGA design on a practical level

*Advanced FPGA Design* Steve Kilts, 2007-06-18 This book provides the advanced issues of FPGA design as the underlying theme of the work In practice an engineer typically needs to be mentored for several years before these principles are appropriately utilized The topics that will be discussed in this book are essential to designing FPGA s beyond moderate complexity The goal of the book is to present practical design techniques that are otherwise only available through mentorship and real world experience

*Computational Science - ICCS 2004* Marian Bubak, Geert D. van Albada, Peter M.A. Sloot, Jack J. Dongarra, 2004-05-13 The International Conference on Computational Science ICCS 2004 held in Krak ow Poland June 6-9 2004 was a follow up to the highly successful ICCS 2003 held at two locations in Melbourne Australia and St Petersburg Russia ICCS 2002 in Amsterdam The Netherlands and ICCS 2001 in San Francisco USA As computational science is still evolving in its quest for subjects of investigation and efficient methods ICCS 2004 was devised as a forum for scientists from mathematics and computer science as the basic computing disciplines and application areas interested in advanced computational methods for physics chemistry life sciences engineering arts and humanities as well as computer system vendors and software developers The main objective of this conference was to discuss problems and solutions in all areas to identify new issues to shape future directions of research and to help users apply various advanced computational techniques The event harvested recent developments in computational grids and next generation computing systems tools advanced numerical methods data driven systems and novel application fields such as complex systems nanotechnology physics and population evolution

*Portable Design*, 2002      **FPGA Prototyping by Verilog Examples** Pong P. Chu, 2011-09-20 FPGA Prototyping Using Verilog Examples will provide you with a hands on introduction to Verilog synthesis and FPGA programming through a learn by doing approach By following the clear easy to understand templates for code development and the numerous practical examples you can quickly develop and simulate a sophisticated digital circuit realize it on a prototyping device and verify the operation of its physical implementation This introductory text that will provide you with a solid foundation instill confidence with rigorous examples for complex systems and prepare you for future development tasks

Parallel Computing is Everywhere S. Bassini, M. Danelutto, P. Dazzi, 2018-03-07 The most powerful computers work by harnessing the combined computational power of millions of processors and exploiting the full potential of such large scale systems is something which becomes more difficult with each succeeding generation of parallel computers Alternative architectures and computer paradigms are increasingly being investigated in an attempt to address these difficulties Added to this the pervasive presence of heterogeneous and parallel devices in consumer products such as mobile phones tablets personal computers and servers also demands efficient programming environments and applications aimed at small scale parallel systems as opposed to large scale supercomputers This book presents a selection of papers presented at the conference Parallel Computing ParCo2017 held in Bologna Italy on 12 to 15 September 2017 The conference included contributions about alternative

approaches to achieving High Performance Computing HPC to potentially surpass exa and zetascale performances as well as papers on the application of quantum computers and FPGA processors These developments are aimed at making available systems better capable of solving intensive computational scientific engineering problems such as climate models security applications and classic NP problems some of which cannot currently be managed by even the most powerful supercomputers available New areas of application such as robotics AI and learning systems data science the Internet of Things IoT and in car systems and autonomous vehicles were also covered As always ParCo2017 attracted a large number of notable contributions covering present and future developments in parallel computing and the book will be of interest to all those working in the field *Advances in Computer Science and Information Engineering* David Jin, Sally Lin, 2012-05-11

CSIE2012 is an integrated conference concentrating its focus on Computer Science and Information Engineering In the proceeding you can learn much more knowledge about Computer Science and Information Engineering of researchers from all around the world The main role of the proceeding is to be used as an exchange pillar for researchers who are working in the mentioned fields In order to meet the high quality of Springer AISC series the organization committee has made their efforts to do the following things Firstly poor quality paper has been refused after reviewing course by anonymous referee experts Secondly periodically review meetings have been held around the reviewers about five times for exchanging reviewing suggestions Finally the conference organizers had several preliminary sessions before the conference Through efforts of different people and departments the conference will be successful and fruitful **Embedded Systems Design with FPGAs** Peter Athanas, Dionisios Pnevmatikatos, Nicolas Sklavos, 2012-12-05 This book presents the methodologies and for embedded systems design using field programmable gate array FPGA devices for the most modern applications Coverage includes state of the art research from academia and industry on a wide range of topics including applications advanced electronic design automation EDA novel system architectures embedded processors arithmetic and dynamic reconfiguration

*Cryptographic Hardware and Embedded Systems - CHES 2000* Cetin K. Koc, Christof Paar, 2000-12-13 This book constitutes the thoroughly refereed post proceedings of the Second International Workshop on Cryptographic Hardware and Embedded Systems CHES 2000 held in Worcester MA USA in August 2000 The 25 revised full papers presented together with two invited contributions were carefully reviewed and selected from 51 submissions The papers are organized in topical sections on implementation of elliptic curve cryptosystems power and timing analysis attacks hardware implementation of block ciphers hardware architectures power analysis attacks arithmetic architectures physical security and cryptanalysis and new schemes and algorithms **IEEE International Conference on Electronics, Circuits and Systems**, 1998

**Modeling, Synthesis, and Rapid Prototyping with the Verilog HDL** Michael D. Ciletti, 1999 Verilog aims to introduce new users to the language of Verilog with instruction on how to write hardware descriptions in Verilog in a style that can be synthesized by readily available synthesis tools Offers clear exposition of the Verilog hardware description

language This book is written in a style that allows the user who has no previous background with hardware description languages HDLs to become skillful with the language Features treatment of synthesis friendly descriptive styles An excellent book for self study reference seminars and workshops on the subject     ASIC & EDA ,1994     Handbook of Research on Advanced Concepts in Real-Time Image and Video Processing Anwar, Md. Imtiyaz,Khosla, Arun,Kapoor, Rajiv,2017-07-13

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