

INDUSTRIAL ELECTRONICS N3

(8080613)

QUESTION 3

APRIL 2021⁻⁸⁻ QUESTION 3

- 3.1 Transistors are not only used in amplifier switch circuits. They may also be used as regulators.



Draw a neat labelled circuit diagram of a transistor used as a constant-current regulator.

(3)

- 3.2 The load line of a common emitter amplifier represents all the possible combinations of collector currents and collector-emitter voltages for amplification. The position of the bias point determines the amplification class.

Describe the following amplification classes.

3.2.1 Class B



3.2.2 Class C

(2 × 2)

(4)

- 3.3 Answer the following questions regarding photo transistors.

3.3.1 Draw a neat, labelled characteristic curve of a photo transistor.

(3)

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China Semiconductor Technology International Conference 2010 (CSTIC 2010) Han-Ming Wu, 2010-03 Our mission is to provide a forum for world experts to discuss technologies address the growing needs associated with silicon technology and exchange their discoveries and solutions for current issues of high interest We encourage collaboration open discussion and critical reviews at this conference Furthermore we hope that this conference will also provide collaborative opportunities for those who are interested in the semiconductor industry in Asia particularly in China **Industrial Electronics**, 1967 *Low-Power Electronics Design* Christian Piguet, 2018-10-03 The power consumption of integrated circuits is one of the most problematic considerations affecting the design of high performance chips and portable devices The study of power saving design methodologies now must also include subjects such as systems on chips embedded software and the future of microelectronics Low Power Electronics Design covers all major aspects of low power design of ICs in deep submicron technologies and addresses emerging topics related to future design This volume explores in individual chapters written by expert authors the many low power techniques born during the past decade It also discusses the many different domains and disciplines that impact power consumption including processors complex circuits software CAD tools and energy sources and management The authors delve into what many specialists predict about the future by presenting techniques that are promising but are not yet reality They investigate nanotechnologies optical circuits ad hoc networks e textiles as well as human powered sources of energy Low Power Electronics Design delivers a complete picture of today s methods for reducing power and also illustrates the advances in chip design that may be commonplace 10 or 15 years from now **Electronics Engineering (O.T.)** R. Kumar, 200? **Electronics, Power Electronics, Optoelectronics, Microwaves, Electromagnetics, and Radar** Richard C. Dorf, 2018-10-03 In two editions spanning more than a decade The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering Our knowledge continues to grow and so does the Handbook For the third edition it has expanded into a set of six books carefully focused on a specialized area or field of study Electronics Power Electronics Optoelectronics Microwaves Electromagnetics and Radar represents a concise yet definitive collection of key concepts models and equations in these areas thoughtfully gathered for convenient access Electronics Power Electronics Optoelectronics Microwaves Electromagnetics and Radar delves into the fields of electronics integrated circuits power electronics optoelectronics electromagnetics light waves and radar supplying all of the basic information required for a deep understanding of each area It also devotes a section to electrical effects and devices and explores the emerging fields of microlithography and power electronics Articles include defining terms references and sources of further information Encompassing the work of the world s foremost experts in their respective specialties Electronics Power Electronics Optoelectronics Microwaves Electromagnetics and Radar features the latest developments the broadest scope of coverage and new material in emerging areas *Oswaal NTA JEE (Main) 10*

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The ever increasing miniaturization of digital electronic components is hampering the conventional testing of Printed Circuit Boards PCBs by means of bed of nails fixtures Basically this is caused by the very high scale of integration of ICs through which packages with hundreds of pins at very small pitches of down to a fraction of a millimetre have become available As a consequence the trace distances between the copper tracks on a printed circuit board come down to the same value Not only the required small physical dimensions of the test nails have made conventional testing unfeasible but also the complexity to provide test signals for the many hundreds of test nails has grown out of limits Therefore a new board test methodology had to be invented Following the evolution in the IC test technology Boundary Scan testing has become the new approach to PCB testing By taking precautions in the design of the IC design for testability testing on PCB level can be simplified to a great extent This condition has been essential for the success of the introduction of Boundary Scan Test BST at board level 100 questions and answers for job interview Offshore Drilling Platforms
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 Low-Power CMOS Circuits Christian Piguet, 2018-10-03 The power consumption of microprocessors is one of the most important challenges of high performance chips and portable devices In chapters drawn from Piguet's recently published *Low Power Electronics Design* Low Power CMOS Circuits Technology Logic Design and CAD Tools addresses the design of low power circuitry in deep submicron technologies It provides a focused reference for specialists involved in designing low power circuitry from transistors to logic gates The book is organized into three broad sections for convenient access The first examines the history of low power electronics along with a look at emerging and possible future technologies It also

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Resources in Education ,1992 **Teaching and Learning in a Digital World** Michael E. Auer,David Guralnick,Istvan Simonics,2018-02-09 This book gathers the Proceedings of the 20th International Conference on Interactive Collaborative Learning ICL2017 held in Budapest Hungary on 27 29 September 2017 The authors are currently witnessing a significant transformation in the development of education The impact of globalisation on all areas of human life the exponential acceleration of technological developments and global markets and the need for flexibility and agility are essential and challenging elements of this process that have to be tackled in general but especially in engineering education To face these current real world challenges higher education has to find innovative ways to quickly respond to them Since its inception in 1998 this conference has been devoted to new approaches in learning with a focus on collaborative learning Today the ICL conferences offer a forum for exchange concerning relevant trends and research results and for sharing practical experience gained while developing and testing elements of new technologies and pedagogies in the learning context

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