An Integrated Embedded System Architecture for In-Vehicle Telematics and Infotainment System

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Abstract—In this study, an integrated embedded system architecture for in-vehicle telematics and infetalmment system (IVTIS) is proposed. A wireless communication network is utilized to provide the location-based and emergency services to the in-vehicle users, while the in-vehicle network is established to provide applications in communications and entertainment. The prototype of the system is demonstrated as well.

I. INTERODUCTION

Traditional on-board embedded electronics systems that performing various operational functions, such as antilock brake, cruise control, climate control, etc... are
already utilized in most of today's modern vehicle [1, 2].
Recent trend shows that more and more in-vehicle
embedded system are focusing on providing better
functionalities, robust operation and higher degree of
convenience to the in-vehicle users. Advance of wireless
communication and information technology in the digital
era has promoted new killer applications to the in-vehicle
drivers and occupants. Among these advanced killer
applications, services provided in the area of the
telematics and information/entertainment have attracted
most attention in the automotive industry.

A. Telematics Service

Telematics were considered as the system that provides location-based services for mobile vehicles over wireless communication networks. Typical example of automotive telematics services are emergency call (or manday) systems, which instantly connects vehicle users to a service center for emergency assistance or roadside services while automatically reporting the vehicle's position [3]. Normally the emergency call system requires a wireless transceiver for voice and data communication and an on-board GPS (Global Positioning System) receiver for positioning. Telematics system was considered as the core technology in an Intelligent Transportation System (ITS) and applications of telematics services to ITS have been proposed recently. An integrated positioning system were developed to realize an efficient and cost-effective GPS based electronic road pricing system by He, Law, and Ling [4]. In [5], the importance of "situational awareness" conveying the state of the automobile to other parties across a communication link was addressed and a novel

interactivity environment for integrated intelligent transportation and telematic systems was proposed.

B. Information/Entertainment Services

As more and more people are traveling with Internetenabled information appliances (IA) such as notebook PCs, PDAs, mobile phones, digital cameras, MP3 players, etc..., there is a desire to connect to the Internet permanently from anywhere, at anytime, without any disruption of service, particularly for those people who spend a significant amount of time in mass transportation systems in weekdays or in their own vehicle during weekend [6]. In order to access the Internet, an in-vehicle local area network or personal area network environment must be established, and the in-vehicle embedded system shall become the mobile gateway for these Internetenabled IA. Ernst, Uehara, and Mitsuya [6], detailed the networking requirements for connecting vehicles to the Internet by displacing an entire IPv6 network and network mobility support in the InternetCAR project. In [7], the software and hardware requirements in designing humancomputer interface for an in-vehicle information system were proposed such that the safety of the in-vehicle drivers is discussed. A distributed service-based architecture [8], based on the Jini middleware technology, were proposed to provide fault tolerant application services to remote in-vehicle computers and mobile devices, such as Wiff-enabled PDA and GPRS phone. It seems that research trend has shifted to providing an infotainment server system for the in-vehicle users such that the network-enabled IA can access the information from the in-vehicle network and also obtain the entertainment services from the entertainment server.

An integrated architecture for in-vehicle telematics and infotainment system (IVTIS) can provide the in-vehicle users value-added services compared with traditional telematics only systems. Thus, an in-vehicle telematics and infotainment system utilizing embedded system with power embedded operation system is proposed in this paper. The packet radio network was adopted to provide the telematics service to the in-vehicle users. And an in-vehicle network environment is established such that a client/server type of in-vehicle infotainment system can be developed. The detailed design and development of the IVTIS will be described as below.

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for Plug and Play Test Automation Krishnendu Chakrabarty, 2013-04-17 System on a Chip SOC integrated circuits composed of embedded cores are now commonplace Nevertheless there remain several roadblocks to rapid and efficient system integration Test development is seen as a major bottleneck in SOC design and manufacturing capabilities Testing SOCs is especially challenging in the absence of standardized test structures test automation tools and test protocols In addition long interconnects high density and high speed designs lead to new types of faults involving crosstalk and signal integrity SOC System on a Chip Testing for Plug and Play Test Automation is an edited work containing thirteen contributions that address various aspects of SOC testing SOC System on a Chip Testing for Plug and Play Test Automation is a valuable reference for researchers and students interested in various aspects of SOC testing VLSI Architecture for Signal, Speech, and Image Processing Durgesh Nandan, Basant Kumar Mohanty, Sanjeev Kumar, Rajeev Kumar Arya,2022-11-03 This new volume introduces various VLSI very large scale integration architecture for DSP filters speech filters and image filters detailing their key applications and discussing different aspects and technologies used in VLSI design models and architectures and more The volume explores the major challenges with the aim to develop real time hardware architecture designs that are compact and accurate It provides useful research in the field of computer arithmetic and can be applied for various arithmetic circuits for their digital implementation schemes and for performance considerations Low Power ECG Processing System for IoT Devices Temesghen Tekeste Habte, Hani Saleh, Baker Mohammad, Mohammed Ismail, 2018-09-06 This book describes an ECG processing architecture that guides biomedical SoC developers from theory to implementation and testing The authors provide complete coverage of the digital circuit implementation of an ultra low power biomedical SoC comprised of a detailed description of an ECG processor implemented and fabricated on chip Coverage also includes the challenges and tradeoffs of designing ECG processors Describes digital circuit architecture for implementing ECG processing algorithms on chip Includes coverage of signal processing techniques for ECG processing Features ultra low power circuit design techniques Enables design of ECG processing architectures and their respective on Mobility-based Time References for Wireless Sensor Networks Fabio Sebastiano, Lucien J. chip implementation Breems, Kofi A Makinwa, 2012-08-09 This book describes the use of low power low cost and extremely small radios to provide essential time reference for wireless sensor networks The authors explain how to integrate such radios in a standard CMOS process to reduce both cost and size while focusing on the challenge of designing a fully integrated time reference for such radios To enable the integration of the time reference system techniques are proposed and analyzed several kinds of integrated time references are reviewed and mobility based references are identified as viable candidates to provide the required accuracy at low power consumption Practical implementations of a mobility based oscillator and a temperature sensor are also presented which demonstrate the required accuracy over a wide temperature range while drawing 51 uW from a 1 2 V supply in a 65 nm CMOS process New methods to engineer and seamlessly reconfigure time triggered

Ethernet based systems during runtime based on the PROFINET IRT example Lukasz Wisniewski, 2017-03-20 The objective of this dissertation is to design a concept that would allow to increase the flexibility of currently available Time Triggered Ethernet based TTEB systems however without affecting their performance and robustness The main challenges are related to scheduling of time triggered communication that may take significant amount of time and has to be performed on a powerful platform Additionally the reliability has to be considered and kept on the required high level Finally the reconfiguration has to be optimally done without affecting the currently running system Nanostructures For Electronics, Photonics, Biosensors, And Emerging Systems Applications Faguir C Jain, C Broadbridge, M Gherasimova, Hong Tang, 2022-11-22 This unique edited compendium consists of peer reviewed articles focusing on 2D materials based nanoelectronics to nanophotonic devices for biosensors and bio nano systems Wide ranging topics span from novel systems for implementing data with security tokens single chemical sensor for multi analyte mixture detection additively manufactured RF devices for communication packaging remote sensing to energy harvesting applications Quantum dot based devices featuring optical modulators and mid infrared photodetectors in the form of Ferroelectric and quantum dot non volatile memories 3D confined quantum dot channel QDC and spatial wavefunction switched SWS FETs for high speed multi bit logic and novel system applications are also included Contributed by eminent researchers recent coverage of materials science for high speed electronics nanoelectronics based on ferroelectric and van der Waals materials material synthesis modeling of dislocations behavior in various heterostructures Ultrahigh Q on chip SiGe microresonators for quantum transduction in new trend in computing are also prominently discussed Readings in Hardware/Software Co-Design Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction ans reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s **Real-Time Simulation Technologies: Principles, Methodologies, and Applications** Katalin Popovici, Pieter Mosterman, 2017-12-19 Real Time Simulation Technologies Principles Methodologies and Applications is an edited compilation of work that explores fundamental concepts and basic techniques of real time simulation for complex and diverse systems across a broad spectrum Useful for both new entrants and experienced experts in the field this book integrates coverage of detailed theory acclaimed methodological approaches entrenched technologies and high value applications of real time simulation all from the unique perspectives of renowned international contributors Because it offers an accurate and otherwise unattainable assessment of how a system will behave over a particular time frame real time simulation is increasingly critical to the optimization of dynamic processes and adaptive systems in a variety of enterprises These range in scope from the maintenance of the national power grid to space exploration to the development of virtual reality programs and cyber physical systems This book outlines how for these and other undertakings engineers must assimilate real time data with computational tools for rapid decision making under uncertainty Clarifying the central concepts behind real time simulation tools and techniques this one

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