

The background of the cover is a high-magnification, false-color micrograph of a microchip die. The die is rectangular and filled with a complex, grid-like pattern of circuitry. Different functional blocks are highlighted in various colors: large green rectangular blocks, smaller red blocks, and purple/blue blocks. Fine, dark lines representing interconnects crisscross the entire surface. The perspective is slightly angled, giving a three-dimensional feel to the flat die.

FOURTH EDITION

CMOS VLSI DESIGN

**A CIRCUITS
AND
SYSTEMS
PERSPECTIVE**

NEIL H. E. WESTE

DAVID MONEY HARRIS

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