

Data Selection 2154.41 s (1 / 31)

Materials Regions Lines/Particles

Name						
Gas		✓	✓		✓	✓
Oxide	✓	✓	✓		✓	✓
Particles	✓	✓	✓	✓	✓	✓
Silicon	✓	✓	✓		✓	✓

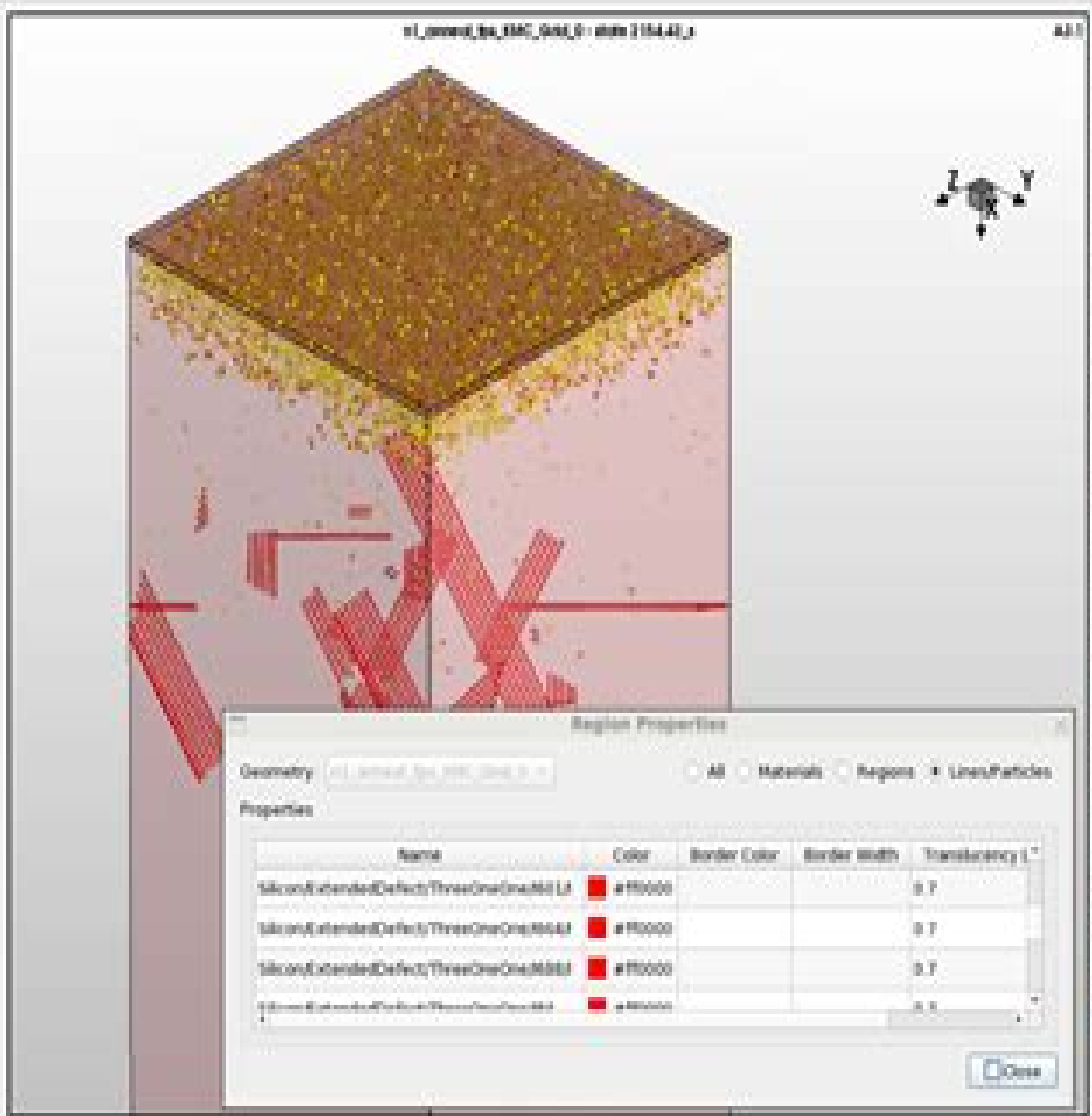
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A/C		
Alloy		
Amorphous		
AmorphousV		
HeatPhase		
Temperature		
X		
Y		
Z		
Index		

Range Levels Label Lines Positions

☐ Fixed

☐ Fixed



Sentaurus Process Manual

Souvik Mahapatra

A red circular graphic with a gradient, appearing as a partial circle or a stylized arrow pointing to the right, located to the right of the author's name.

Sentaurus Process Manual:

Simulation of Semiconductor Processes and Devices 2007 Tibor Grasser, 2007-09-18 This volume contains the proceedings of the 12th International Conference on Simulation of Semiconductor Processes and Devices SISPAD 2007 held September 2007 in Vienna Austria It provides a global forum for the presentation and discussion of recent advances and developments in the theoretical description physical modeling and numerical simulation and analysis of semiconductor fabrication processes device operation and system performance

Nanoelectronics: Physics, Materials and Devices

Angsuman Sarkar, Chandan Kumar Sarkar, Arpan Deyasi, Debashis De, Arezki Benfdila, 2023-01-03 Approx 528 pages Approx 528 pages

Nanowires Paola Prete, 2010-02-01 This volume is intended to orient the reader in the fast developing field of semiconductor nanowires by providing a series of self contained monographs focusing on various nanowire related topics Each monograph serves as a short review of previous results in the literature and description of methods used in the field as well as a summary of the authors recent achievements on the subject Each report provides a brief sketch of the historical background behind the physical and or chemical principles underlying a specific nanowire fabrication characterization technique or the experimental theoretical methods used to study a given nanowire property or device Despite the diverse topics covered the volume does appear as a unit The writing is generally clear and precise and the numerous illustrations provide an easier understanding of the phenomena described The volume contains 20 Chapters covering altogether many although not all semiconductors of technological interest starting with the IV IV group compounds SiC and SiGe carrying on with the binary and ternary compounds of the III V GaAs AlGaAs GaSb InAs GaP InP and GaN and II VI HgTe HgCdTe families the metal oxides CuO ZnO ZnCoO tungsten oxide and PbTiO₃ and finishing with Bi a semimetal

Strain-Engineered MOSFETs C.K. Maiti, T.K. Maiti, 2018-10-03 Currently strain engineering is the main technique used to enhance the performance of advanced silicon based metal oxide semiconductor field effect transistors MOSFETs Written from an engineering application standpoint Strain Engineered MOSFETs introduces promising strain techniques to fabricate strain engineered MOSFETs and to methods to assess the applications of these techniques The book provides the background and physical insight needed to understand new and future developments in the modeling and design of n and p MOSFETs at nanoscale This book focuses on recent developments in strain engineered MOSFETs implemented in high mobility substrates such as Ge SiGe strained Si ultrathin germanium on insulator platforms combined with high k insulators and metal gate It covers the materials aspects principles and design of advanced devices fabrication and applications It also presents a full technology computer aided design TCAD methodology for strain engineering in Si CMOS technology involving data flow from process simulation to process variability simulation via device simulation and generation of SPICE process compact models for manufacturing for yield optimization Microelectronics fabrication is facing serious challenges due to the introduction of new materials in manufacturing and fundamental limitations of nanoscale devices that result in increasing unpredictability in

the characteristics of the devices The down scaling of CMOS technologies has brought about the increased variability of key parameters affecting the performance of integrated circuits This book provides a single text that combines coverage of the strain engineered MOSFETs and their modeling using TCAD making it a tool for process technology development and the design of strain engineered MOSFETs High Performance CMOS Range Imaging Andreas Süss,2016-03-24 This work is dedicated to CMOS based imaging with the emphasis on the noise modeling characterization and optimization in order to contribute to the design of high performance imagers in general and range imagers in particular CMOS is known to be superior to CCD due to its flexibility in terms of integration capabilities but typically has to be enhanced to compete at parameters as for instance noise dynamic range or spectral response This work gathers the widespread theory on noise and extends the theory by a non rigorous but potentially computing efficient algorithm to estimate noise in time sampled systems

Introducing Technology Computer-Aided Design (TCAD) Chinmay K. Maiti,2017-03-16 This might be the first book that deals mostly with the 3D technology computer aided design TCAD simulations of major state of the art stress and strain engineered advanced semiconductor devices MOSFETs BJTs HBTs nonclassical MOS devices finFETs silicon germanium hetero FETs solar cells power devices and memory devices The book focuses on how to set up 3D TCAD simulation tools from mask layout to process and device simulation including design for manufacturing DFM and from device modeling to SPICE parameter extraction The book also offers an innovative and new approach to teaching the fundamentals of semiconductor process and device design using advanced TCAD simulations of various semiconductor structures The simulation examples chosen are from the most popular devices in use today and provide useful technology and device physics insights To extend the role of TCAD in today s advanced technology era process compact modeling and DFM issues have been included for design technology interface generation Unique in approach this book provides an integrated view of silicon technology and beyond with emphasis on TCAD simulations It is the first book to provide a web based online laboratory for semiconductor device characterization and SPICE parameter extraction It describes not only the manufacturing practice associated with the technologies used but also the underlying scientific basis for those technologies Written from an engineering standpoint this book provides the process design and simulation background needed to understand new and future technology development process modeling and design of nanoscale transistors The book also advances the understanding and knowledge of modern IC design via TCAD improves the quality in micro and nanoelectronics R D and supports the training of semiconductor specialists It is intended as a textbook or reference for graduate students in the field of semiconductor fabrication and as a reference for engineers involved in VLSI technology development who have to solve device and process problems CAD specialists will also find this book useful since it discusses the organization of the simulation system in addition to presenting many case studies where the user applies TCAD tools in different situations **Nanowires** Simas Rackauskas,2019-04-10 Nanowires are attracting wide scientific interest due to the unique properties associated with their one dimensional

geometry Developments in the understanding of the fundamental principles of the nanowire growth mechanisms and mastering functionalization provide tools to control crystal structure morphology and the interactions at the material interface and create characteristics that are superior to those of planar geometries This book provides a comprehensive overview of the most important developments in the field of nanowires starting from their synthesis discussing properties and finalizing with nanowire applications The book consists of two parts the first is devoted to the synthesis of nanowires and characterization and the second investigates the properties of nanowires and their applications in future devices **CMOS**

Nanoelectronics Nadine Collaert,2012-09-19 This book covers one of the most important device architectures that have been widely researched to extend the transistor scaling FinFET Starting with theory the book discusses the advantages and the integration challenges of this device architecture It addresses in detail the topics such as high density fin patterning gate stack design and s *Hot Carrier Degradation in Semiconductor Devices* Tibor Grassner,2014-10-29 This book provides

readers with a variety of tools to address the challenges posed by hot carrier degradation one of today s most complicated reliability issues in semiconductor devices Coverage includes an explanation of carrier transport within devices and book keeping of how they acquire energy become hot interaction of an ensemble of colder and hotter carriers with defect precursors which eventually leads to the creation of a defect and a description of how these defects interact with the device degrading its performance *Graphene, Ge/III-V, and Emerging Materials for Post-CMOS Applications 2 P.*

Srinivasan,2010-04 This issue of ECS Transactions addresses the fundamental material science characterization modeling and applications of Graphene Ge III V and Emerging materials designed for alternatives technologies to replace CMOS

Doping Engineering for Front-End Processing: Volume 1070 Materials Research Society. Meeting Symposium E.,2008-10-17 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners 3D TCAD Simulation for CMOS Nanoelectronic Devices Yung-Chun Wu,Yi-Ruei Jhan,2017-06-19 This book

demonstrates how to use the Synopsys Sentaurus TCAD 2014 version for the design and simulation of 3D CMOS complementary metal oxide semiconductor semiconductor nanoelectronic devices while also providing selected source codes Technology Computer Aided Design TCAD Instead of the built in examples of Sentaurus TCAD 2014 the practical cases presented here based on years of teaching and research experience are used to interpret and analyze simulation results of the physical and electrical properties of designed 3D CMOSFET metal oxide semiconductor field effect transistor nanoelectronic devices The book also addresses in detail the fundamental theory of advanced semiconductor device design for the further simulation and analysis of electric and physical properties of semiconductor devices The design and simulation technologies for nano semiconductor devices explored here are more practical in nature and representative of the semiconductor industry and as such can promote the development of pioneering semiconductor devices semiconductor device physics and more practically oriented approaches to teaching and learning semiconductor engineering The book can

be used for graduate and senior undergraduate students alike while also offering a reference guide for engineers and experts in the semiconductor industry Readers are expected to have some preliminary knowledge of the field

Computer Aided Design Of Micro- And Nanoelectronic Devices Chinmay Kumar Maiti, 2016-10-27 Micro and nanoelectronic devices are the prime movers for electronics which is essential for the current information age This unique monograph identifies the key stages of advanced device design and integration in semiconductor manufacturing It brings into one resource a comprehensive device design using simulation The book presents state of the art semiconductor device design using the latest TCAD tools Professionals researchers academics and graduate students in electrical electronic engineering and microelectronics will benefit from this reference text

Disruptive Wide Bandgap Semiconductors, Related Technologies, and Their Applications Yogesh Kumar Sharma, 2018-09-12 SiC and GaN devices have been around for some time The first dedicated international conference on SiC and related devices ICSCRM was held in Washington DC in 1987 But only recently the commercialization of SiC and GaN devices has happened Due to its material properties Si as a semiconductor has limitations in high temperature high voltage and high frequency regimes With the help of SiC and GaN devices it is possible to realize more efficient power systems Devices manufactured from SiC and GaN have already been impacting different areas with their ability to outperform Si devices Some of the examples are the telecommunications automotive locomotive power and renewable energy industries To achieve the carbon emission targets set by different countries it is inevitable to use these new technologies This book attempts to cover all the important facets related to wide bandgap semiconductor technology including new challenges posed by it This book is intended for graduate students researchers engineers and technology experts who have been working in the exciting fields of SiC and GaN power devices

Recent Advances in PMOS Negative Bias Temperature Instability Souvik Mahapatra, 2021-11-25 This book covers advances in Negative Bias Temperature Instability NBTI and will prove useful to researchers and professionals in the semiconductor devices areas NBTI continues to remain as an important reliability issue for CMOS transistors and circuits Development of NBTI resilient technology relies on utilizing suitable stress conditions artifact free measurements and accurate physics based models for the reliable determination of degradation at end of life as well as understanding the process material and device architectural impacts This book discusses Ultra fast measurements and modelling of parametric drift due to NBTI in different transistor architectures planar bulk and FDSOI p MOSFETs p FinFETs and GAA SNS p FETs with Silicon and Silicon Germanium channels BTI Analysis Tool BAT a comprehensive physics based framework to model the measured time kinetics of parametric drift during and after DC and AC stress at different stress and recovery biases and temperature as well as pulse duty cycle and frequency The Reaction Diffusion RD model is used for generated interface traps Transient Trap Occupancy Model TTOM for charge occupancy of the generated interface traps and their contribution Activated Barrier Double Well Thermionic ABDWT model for hole trapping in pre existing bulk gate insulator traps and Reaction Diffusion Drift RDD model

for bulk trap generation in the BAT framework NBTI parametric drift is due to uncorrelated contributions from the trap generation interface bulk and trapping processes Analysis and modelling of Nitrogen incorporation into the gate insulator Germanium incorporation into the channel and mechanical stress effects due to changes in the transistor layout or device dimensions similarities and differences of 100 surface dominated planar and GAA MOSFETs and 110 sidewall dominated FinFETs are analysed

Reliability of High Mobility SiGe Channel MOSFETs for Future CMOS Applications Jacopo Franco,Ben Kaczer,Guido Groeseneken,2013-10-19 Due to the ever increasing electric fields in scaled CMOS devices reliability is becoming a showstopper for further scaled technology nodes Although several groups have already demonstrated functional Si channel devices with aggressively scaled Equivalent Oxide Thickness EOT down to 5 a 10 year reliable device operation cannot be guaranteed anymore due to severe Negative Bias Temperature Instability This book focuses on the reliability of the novel Si Ge channel quantum well pMOSFET technology This technology is being considered for possible implementation in next CMOS technology nodes thanks to its benefit in terms of carrier mobility and device threshold voltage tuning We observe that it also opens a degree of freedom for device reliability optimization By properly tuning the device gate stack sufficiently reliable ultra thin EOT devices with a 10 years lifetime at operating conditions are demonstrated The extensive experimental datasets collected on a variety of processed 300mm wafers and presented here show the reliability improvement to be process and architecture independent and as such readily transferable to advanced device architectures as Tri Gate finFET devices We propose a physical model to understand the intrinsically superior reliability of the MOS system consisting of a Ge based channel and a SiO₂ HfO₂ dielectric stack The improved reliability properties here discussed strongly support Si Ge technology as a clear frontrunner for future CMOS technology nodes

SiGe and Ge David Louis Harame,2006 The second International SiGe Ge Materials Processing and Devices Symposium was part of the 2006 ECS conference held in Cancun Mexico from October 29 Nov 3 2006 This meeting provided a forum for reviewing and discussing all materials and device related aspects of SiGe Ge The hardcover edition includes a bonus CD ROM containing the PDF of the entire issue

Microelectronics Technology and Devices - SBMicro 2008 Jacobus W. Swart,2008-08 The SBMicro symposium is a forum dedicated to fabrication and modeling of microsystems integrated circuits and devices The goal of the symposium is to bring together researchers in the areas of processing materials characterization modeling and TCAD of integrated circuits microsensors microactuators and MEMS This issue of ECS Transactions contains the papers presented at the 2008 conference

Технология, конструкции и методы моделирования кремниевых интегральных микросхем. Часть 2 Михаил Королев,Татьяна Крупкина,Михаил Путря,Василий Шевяков,2014-02-07

Semiconductor Defect Engineering: Volume 994 S. Ashok,2007-09-10 The MRS Symposium Proceeding series is an internationally recognised reference suitable for researchers and practitioners This book first published in 2007 focuses on the application of defects and impurities in current and emerging semiconductor technologies

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