

First Second Semester B.E./B.Tech. Degree Examination, June/July 2023 Basic Electronics for EEE Stream

Max. Marks: 100

- Note: 1. Answer any FIVE full questions, choosing ONE full question from each module.
 - 2. M : Marks , L: Bloom's level , C: Course outcomes.
 - 3. Assume missed data.

		Module = 1	NI	L	C
Q.1	a.	Explain forward and reverse characteristics of semiconductor diode.	8	L.2	COI
	b.	Calculate forward and reverse resistances offered by a silicon diode with $I_f=100 mA$ at $V_R=50 V_s$. Assume V_F for silicon diode to be 0.75V and reverse current $I_R=100 nA$.	4	L.3	COI
	c.	What is piecewise linear characteristic? With neat diagram explain diode approximation of Ideal diode and practical diode.	8	L2	COI
		OR	- 8	12	COL
Q.2	a.	Describe the working of full wave bridge rectifier.	-8	hos	
	ъ.	Explain zener diode as voltage regulator with no load and with load.	6	12	COI
	c.	Illustrate RC- π filter.	6	L.2	COI
	-	Module - 2			
Q.3	at.	Explain output characteristics of a transistor in common base configuration.	6	L.2	CO2
	ь.	Describe the procedure for drawing dc - load line on transistor CE output characteristics.	8	L.2	CO2
	c.	Calculate I_e and I_B for a transistor that has $\alpha_{DC}=0.98$, $I_B=100\mu A$. Also determine the value of β_{DC} for the transistor.	6	L.3	CO2
	-	OR			
Q.4	a.	Explain common Emitter input characteristics.	6	L.2	CO2
	ь.	Explain how transistor can be used as current amplifier.	6	12	CO2
	e.	Explain the working of N-channel JFET.	8	1.2	CO2

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web 240 holt mcdougal geometry end of course test 23 the figure represents a rectangular gate with diagonal braces to the nearest tenth what is the width qt of the gate f 15 21 h a 3 9 ft c 7 0 ft b 4 9 ft d c7 6 ft refer to the figure for exercises 24 and 25 f 15 ft h 24 kim is making a kite with a wooden