

CBGS SCHEME

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18EVE14

First Semester M.Tech. Degree Examination, Dec.2018/Jan.2019

VLSI Testing

Time: 3 hrs.

Max. Marks: 100

Note: Answer any FIVE full questions, choosing ONE full question from each module.

Module-1

- 1
 - a. What is failure and fault? Explain any two fault models in detail by specifying its characteristics. (12 Marks)
 - b. Explain temporary fault in brief. (04 Marks)
 - c. What are the different types of simulation? (04 Marks)

OR

- 2
 - a. What is logic simulation? Explain the problems in simulation based design verification. (07 Marks)
 - b. Explain different types of Delay models. (07 Marks)
 - c. Explain Event Evaluation and Hazard detection. (06 Marks)

Module-2

- 3
 - a. What is fault diagnosis? Explain one dimensional path sensitization technique for combination circuits with an example. (08 Marks)
 - b. Consider the logic circuit shown in Fig.Q3(b). Find the Boolean difference with respect to x_2 . (06 Marks)



Fig.Q3(b)

- c. Explain PODEM algorithm with brief explanation. (06 Marks)
- OR
- 4
 - a. Explain Reed-Muller Expansion technique in detail. (10 Marks)
 - b. Write short notes on:
 - i) Three-level OR-AND-OR design
 - ii) Automatic synthesis of testable logic
 (10 Marks)

Module-3

- 5
 - a. Explain different types of extraction for testing of multilevel combination circuit. (09 Marks)
 - b. Explain Demorgan's transformation with an example. (06 Marks)
 - c. Explain path delay faults in 2-level OR-AND circuit for testing purpose. (05 Marks)

Testing Of Vlsi Previous Question Paper

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Assessing Fault Model and Test Quality Kenneth M. Butler, M. Ray Mercer, 2012-12-06 For many years the dominant fault model in automatic test pattern generation ATPG for digital integrated circuits has been the stuck at fault model The static nature of stuck at fault testing when compared to the extremely dynamic nature of integrated circuit IC technology has caused many to question whether or not stuck at fault based testing is still viable Attempts at answering this question have not been wholly satisfying due to a lack of true quantification statistical significance and or high computational expense In this monograph we introduce a methodology to address the question in a manner which circumvents the drawbacks of previous approaches The method is based on symbolic Boolean functional analyses using Ordered Binary Decision Diagrams OBDDs OBDDs have been conjectured to be an attractive representation form for Boolean functions although cases exist for which their complexity is guaranteed to grow exponentially with input cardinality Classes of Boolean functions which exploit the efficiencies inherent in OBDDs to a very great extent are examined in Chapter 7 Exact equations giving their OBDD sizes are derived whereas until very recently only size bounds have been available These size equations suggest that straightforward applications of OBDDs to design and test related problems may not prove as fruitful as was once thought **SBI**

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Verification, Model Checking, and Abstract Interpretation Bernhard Steffen, Giorgio Levi, 2003-12-19 This volume contains the proceedings of the 5th International Conference on Verification Model Checking and Abstract Interpretation VMCAI 2004 held in Venice January 11-13 2004 in conjunction with POPL 2004 the 31st Annual Symposium on Principles of Programming Languages January 14-16 2004 The purpose of VMCAI is to provide a forum for researchers from three communities verification model checking and abstract interpretation which will facilitate interaction cross fertilization and the advance of hybrid methods that combine the three areas With the growing need for formal tools to reason about complex in nite state and embedded systems such hybrid methods are bound to be of great importance Topics covered by VMCAI include program verification static analysis techniques model checking program certification type systems abstract domains debugging techniques compiler optimization embedded systems and formal analysis of security protocols This year's meeting follows the four previous events in Port Jefferson 1997 Pisa 1998 Venice 2002 LNCS 2294 and New York 2003 LNCS 2575 In particular we thank VMCAI 2003's sponsor the Courant Institute at New York University for allowing us to apply a monetary surplus from the 2003 meeting to this one The program committee selected 22 papers out of 68 on the basis of three views The principal criteria were relevance and quality The program of VMCAI 2004 included in addition to the research papers a keynote speech by David Harel Weizmann Institute Israel on A Grand Challenge for Computing Full Reactive Modeling of a Multicellular Animal an invited talk by Dawson Engler Stanford University USA on Static Analysis Versus Software Model Checking for Bug Finding an invited talk by Mooly Sagiv Tel

Aviv University Israel called On the Expressive Power of Canonical Abstraction and a tutorial by Joshua D Guttman MITRE USA on Security Protocols and Trust We would like to thank the Program Committee members and the reviewers without whose dedicated effort the conference would not have been possible *Digest of Papers : FTCS ,1982* [Digest of Technical Papers ,1999](#) **IB ACIO Grade II/Executive Exam (English Edition) - 10 Practice Tests and 5 Previous Year Papers (1500 Solved Objective Questions)** EduGorilla Prep Experts,2022-12-06 Best Selling Book in English Edition for IB ACIO Grade II Executive Exam with objective type questions as per the latest syllabus given by the Ministry of Home Affairs MHA IB ACIO Grade II Executive Exam Preparation Kit comes with 10 Practice Tests and 5 Previous Year Papers with the best quality content Increase your chances of selection by 16X IB ACIO Grade II Executive Exam Prep Kit comes with well structured and 100% detailed solutions for all the questions Clear exam with good grades using thoroughly Researched Content by experts [UPSSSC VDO Exam : Village Development/Panchayat Officer & Samaj Kalyan Paryavekshak | 6 Mock Tests + 3 Sectional Tests + 1 Previous Year Paper \(1200+ Solved Questions\)](#) EduGorilla Prep Experts,2022-09-15 Best Selling Book in English Edition for UPSSSC VDO Exam with objective type questions as per the latest syllabus given by the UPSSSC Compare your performance with other students using Smart Answer Sheets in EduGorilla s UPSSSC VDO Exam Practice Kit UPSSSC VDO Exam Preparation Kit comes with 10 Tests 6 Mock Tests 3 Sectional Tests 1 Previous Year Paper with the best quality content Increase your chances of selection by 16X UPSSSC VDO Exam Prep Kit comes with well structured and 100% detailed solutions for all the questions Clear exam with good grades using thoroughly Researched Content by experts *SBI Clerk Junior Associates Prelims Exam 2024 (English Edition) - 8 Mock Tests, 9 Sectional Tests and 3 Previous Year Papers (1400 Solved Questions)* EduGorilla Prep Experts,2022-12-20 Best Selling Book in English Edition for SBI Clerk Junior Associates Prelims Exam with objective type questions as per the latest syllabus given by the SBI SBI Clerk Junior Associates Prelims Exam Preparation Kit comes with 20 Tests 8 Mock Tests 9 Sectional Tests 3 Previous Year Papers with the best quality content Increase your chances of selection by 16X SBI Clerk Junior Associates Prelims Exam Prep Kit comes with well structured and 100% detailed solutions for all the questions Clear exam with good grades using thoroughly Researched Content by experts *Mathematical Foundations of Computer Science 1986* Jozef Gruska,Branislav Rován,Juraj Wiedermann,1986-08-01 [Communicating Process Architectures 2017 & 2018](#) J. Bækgaard Pedersen,K. Chalmers,J.F. Broenink,2019-03-26 Concurrent and parallel systems are intrinsic to the technology which underpins almost every aspect of our lives today This book presents the combined post proceedings for two important conferences on concurrent and parallel systems Communicating Process Architectures 2017 held in Sliema Malta in August 2017 and Communicating Process Architectures 2018 held in Dresden Germany in August 2018 CPA 2017 Fifteen papers were accepted for presentation and publication they cover topics including mathematical theory programming languages design and support tools verification and multicore infrastructure and applications ranging from supercomputing to embedded A

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System-on-Chip Test Architectures Laung-Terng Wang, Charles E. Stroud, Nur A. Touba, 2010-07-28 Modern electronics testing has a legacy of more than 40 years The introduction of new technologies especially nanometer technologies with 90nm or smaller geometry has allowed the semiconductor industry to keep pace with the increased performance capacity demands from consumers As a result semiconductor test costs have been growing steadily and typically amount to 40% of today s overall product cost This book is a comprehensive guide to new VLSI Testing and Design for Testability techniques that will allow students researchers DFT practitioners and VLSI designers to master quickly System on Chip Test architectures for test debug and diagnosis of digital memory and analog mixed signal designs Emphasizes VLSI Test principles and Design for Testability architectures with numerous illustrations examples Most up to date coverage available including Fault Tolerance Low Power Testing Defect and Error Tolerance Network on Chip NOC Testing Software Based Self Testing FPGA Testing MEMS Testing and System In Package SIP Testing which are not yet available in any testing book Covers the entire spectrum of VLSI testing and DFT architectures from digital and analog to memory circuits and fault diagnosis and self repair from digital to memory circuits Discusses future nanotechnology test trends and challenges facing the nanometer design era promising nanotechnology test techniques including Quantum Dots Cellular Automata Carbon Nanotubes and Hybrid Semiconductor Nanowire Molecular Computing Practical problems at the end of each chapter for students

Fault Detection & Reliability M.G. Singh, 2013-10-22 Provides an up to date review of the latest developments in system reliability maintenance fault detection and fault tolerant design techniques Topics covered include reliability analysis and optimization maintenance control policies fault detection techniques fault tolerant systems reliable controllers and robustness knowledge based approaches and decision support systems There are further applications papers on process control robotics manufacturing systems communications and power systems Contains 36 papers

Large Space Structures & Systems in the Space Station Era, 1991

Basic Electronics Rakesh Kumar Garg, Ashish Dixit, Pavan Yadav, 2008

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many of the characters who make appearances in spoon river anthology were based on people that masters knew or heard of in the two towns in which he grew up petersburg and lewistown illinois masters sometimes substantially disguised the names of these real life inspirations but he sometimes disguised them only barely and in a few cases not at all most notable is anne rutledge regarded in local legend to be abraham lincoln s early love interest though there is n

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with the body in a prone position i warmed the musculature of the back using the classical massage movements of effleurage and then kneading the soft tissue as preparation see more

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cc has a curvature and tightness of the spine causing a lot of pain the osteopath referred her to me to keep her musculature in better condition making it easier to manipulate and to maintain improvements after osteopathic treatment i see cc on a regular basis weekly or fortnightly cc is of a slim build and 5 8 see more

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the aim was to release the tension in the muscles of the lower and upper back and neck using palpation to find tight areas and points of micro trauma i treated using neuromuscular see more

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web a sample outline is shown here show them what inspired you to get into this career and show them what inspires you to work towards this goal while proposals don t always

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several stretches were shown to be done mornings and evenings i suggested side stretches for the quadratus lumborum a couple of stretches for the rhomboids and the neck to help maintain the improvement and to keep the see more

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discussed that it would be a sports massage treatment