

VHDL-AMS Quick Reference



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IEEE VHDL-AMS 1076.1 is the premier industry standard mixed-signal high-level description language for electronic and multi-domain systems. This short reference describes the principal features of the language. Complete information is contained in the IEEE publication *IEEE Standard VHDL Analog and Mixed-Signal Extensions*, ISBN 0-7381-1640-8, from which this summary is derived.

The left hand column of each table is a general description and the right hand column contains explanations or examples. The following syntax conventions are used:

entity	reserved words	operand (operation operand)	repeated items
[expression]	optional item	identifier [. . .]	repeated items (dot notation)
letter digit	alternative selection	:=	production rule

Design Units

[context_clause] entity entity_name is [generic (parameter_list) ;] [port (port_list) ;] [declaration_part] begin [passive_concurrent_statement] ; end [entity] [entity_name] ;	library DISCIPLINES; use DISCIPLINES.ELECTRICAL_SYSTEMS.all; entity comparator is generic (level : REAL := 2.5); port (threshold_a, ref : ELECTRICAL; signal_d : out BIT); begin assert level > 0.0 report "Level must be >0.0" severity ERROR; end entity comparator;
[context_clause] architecture architecture_name of entity_name is [declaration_part] begin [simultaneous_statement] [concurrent_statement] ; end [architecture] [architecture_name] ;	architecture simple of comparator is quantity x across i through a to ref; begin v <== 1.0E6*v _i ; d <= '1' when x'ABOVE(level) else '0'; end architecture simple ;
[context_clause] package package_name is declaration [. . .] end [package] [package_name] ; [context_clause] package body package_name is declaration [. . .] end [packagebody] [package_name] ;	package my_functions is function boolean2bit (b: BOOLEAN) return BIT; end package my_functions; package body my_functions is function boolean2bit (b:BOOLEAN) return BIT is begin if b = TRUE then return '1'; else return '0'; end; end function boolean2bit; end package my_functions;

Vhdl Ams Language Reference Manual

Ken Kundert, Olaf Zinke



Vhdl Ams Language Reference Manual:

The System Designer's Guide to VHDL-AMS Peter J. Ashenden, Gregory D. Peterson, Darrell A. Teegarden, 2002-09-10
The demand is exploding for complete integrated systems that sense process manipulate and control complex entities such as sound images text motion and environmental conditions These systems from hand held devices to automotive sub systems to aerospace vehicles employ electronics to manage and adapt to a world that is predominantly neither digital nor electronic To respond to this design challenge the industry has developed and standardized VHDL AMS a unified design language for modeling digital analog mixed signal and mixed technology systems VHDL AMS extends VHDL to bring the successful HDL modeling methodology of digital electronic systems design to these new design disciplines Gregory Peterson and Darrell Teegarden join best selling author Peter Ashenden in teaching designers how to use VHDL AMS to model these complex systems This comprehensive tutorial and reference provides detailed descriptions of both the syntax and semantics of the language and of successful modeling techniques It assumes no previous knowledge of VHDL but instead teaches VHDL and VHDL AMS in an integrated fashion just as it would be used by designers of these complex integrated systems Explores the design of an electric powered unmanned aerial vehicle system UAV in five separate case studies to illustrate mixed signal mixed technology power systems communication systems and full system modeling Approved Draft Standard VHDL Language Reference Manual (integrated with VHDL-AMS Changes) Computer Society, Institute of Electrical and Electronics Engineers, 1999

The Designer's Guide to Verilog-AMS Ken Kundert, Olaf Zinke, 2005-12-19 The Verilog Hardware Description Language Verilog HDL has long been the most popular language for describing complex digital hardware It started life as a proprietary language but was donated by Cadence Design Systems to the design community to serve as the basis of an open standard That standard was formalized in 1995 by the IEEE in standard 1364 1995 About that same time a group named Analog Verilog International formed with the intent of proposing extensions to Verilog to support analog and mixed signal simulation The first fruits of the labor of that group became available in 1996 when the language definition of Verilog A was released Verilog A was not intended to work directly with Verilog HDL Rather it was a language with Similar syntax and related semantics that was intended to model analog systems and be compatible with SPICE class circuit simulation engines The first implementation of Verilog A soon followed a version from Cadence that ran on their Spectre circuit simulator As more implementations of Verilog A became available the group defining the a log and mixed signal extensions to Verilog continued their work releasing the definition of Verilog AMS in 2000 Verilog AMS combines both Verilog HDL and Verilog A and adds additional mixed signal constructs providing a hardware description language suitable for analog digital and mixed signal systems Again Cadence was first to release an implementation of this new language in a product named AMS Designer that combines their Verilog and Spectre simulation engines

Mixed-Signal Methodology Guide Jess Chen, Michael Henrie, Monte F. Mar, Ph.D., Mladen Nizic, 2012 This book the Mixed signal Methodology Guide

Advanced Methodology for AMS IP and SoC Design Verification and Implementation provides a broad overview of the design verification and implementation methodologies required for today's mixed signal designs. The book covers mixed signal design trends and challenges, abstraction of analog using behavioral models, assertion based metric driven verification methodology applied on analog and mixed signal, and verification of low power intent in mixed signal design. It also describes methodology for physical implementation in context of concurrent mixed signal design and for handling advanced node physical effects. The book contains many practical examples of models and techniques. The authors believe it should serve as a reference to many analog, digital, and mixed signal designers, verification, physical implementation engineers, and managers in their pursuit of information for a better methodology required to address the challenges of modern mixed signal design.

System on Chip Design Languages Anne Mignotte, Eugenio Villar, Lynn Horobin, 2013-04-17 This book is the third in a series of books collecting the best papers from the three main regional conferences on electronic system design languages: HDLCon in the United States, APCHDL in Asia Pacific, and FDL in Europe. Being APCHDL bi-annual, this book presents a selection of papers from HDLCon 01 and FDL 01. HDLCon is the premier HDL event in the United States. It originated in 1999 from the merging of the International Verilog Conference and the Spring VHDL User's Forum. The scope of the conference expanded from specialized languages such as VHDL and Verilog to general purpose languages such as C and Java. In 2001 it was held in February in Santa Clara, CA. Presentations from design engineers are technical in nature, reflecting real life experiences in using HDLs. EDA vendors' presentations show what is available and what is planned for design tools that utilize HDLs such as simulation and synthesis tools. The Forum on Design Languages (FDL) is the European forum to exchange experiences and learn of new trends in the application of languages and the associated design methods and tools to design complex electronic systems. FDL 01 was held in Lyon, France, around seven interrelated workshops: Hardware Description Languages, Analog and Mixed signal Specification, C/C++ HW/SW Specification and Design, Design Environments, Languages, Real Time specification for embedded Systems, Architecture Modeling and Reuse, and System Specification. Design Languages.

Advances in Design and Specification Languages for SoCs Pierre Boulet, 2006-06-30 The seventh book in the CHDL Series is composed of a selection of the best articles from the Forum on Specification and Design Languages (FDL) 04. FDL is the European Forum to learn and exchange on new trends on the application of languages and models for the design of electronic and heterogeneous systems. The forum was structured around four workshops that are all represented in the book by outstanding articles: Analog and Mixed Signal Systems, UML based System Specification and Design, C/C++ Based System Design and Languages for Formal Specification and Verification. The Analog and Mixed Signal Systems contributions bring some answers to the difficult problem of co-simulating discrete and continuous models of computation. The UML based System Specification and Design chapters bring insight into how to use the Model Driven Engineering to design Systems on Chip. The C/C++ Based System Design articles mainly explore system level design with SystemC. The Languages for

Formal Specification and Verification is represented by an invited contribution on the use of temporal assertions for symbolic model checking and simulation And finally chapter in this book contributed by preeminent members of the automotive design industry presents the recent industry standard AutoSAR Overall Advances in Design and Specification Languages for SoCs is an excellent opportunity to catch up with the latest research developments in the field of languages for electronic and heterogeneous system design

The Mechatronics Handbook - 2 Volume Set Robert H. Bishop, 2018-10-08 The first comprehensive reference on mechatronics The Mechatronics Handbook was quickly embraced as the gold standard in the field From washing machines to coffeemakers to cell phones to the ubiquitous PC in almost every household what these days doesn't take advantage of mechatronics in its design and function In the scant five years since the initial publication of the handbook the latest generation of smart products has made this even more obvious Too much material to cover in a single volume Originally a single volume reference the handbook has grown along with the field The need for easy access to new material on rapid changes in technology especially in computers and software has made the single volume format unwieldy The second edition is offered as two easily digestible books making the material not only more accessible but also more focused Completely revised and updated Robert Bishop's seminal work is still the most exhaustive state of the art treatment of the field available

System Specification and Design Languages Tom J. Kaźmierski, Adam Morawiec, 2011-12-02 This book brings together a selection of the best papers from the thirteenth edition of the Forum on specification and Design Languages Conference FDL which was held in Southampton UK in September 2010 FDL is a well established international forum devoted to dissemination of research results practical experiences and new ideas in the application of specification design and verification languages to the design modelling and verification of integrated circuits complex hardware software embedded systems and mixed technology systems

A Practical Guide to Analog Behavioral Modeling for IC System Design Paul A. Duran, 2012-11-14 A Practical Guide to Analog Behavioral Modeling for IC System Design presents a methodology for abstracting an IC system so that the designer can gain a macroscopic view of how sub systems interact as well as verify system functionality in various applications before committing to a design This will prevent problems that may be caused late in the design cycle by incompatibilities between the individual blocks that comprise the overall system This book will focus on the techniques of modelling IC systems through analog behavioral modeling and simulation It will investigate a practical approach by which designers can put together these systems to analyze topological and architectural issues to optimize IC system performance Highlights Discussions on modeling and simulation from SPICE to behavioral simulators Comparison of various hardware description languages and a discussion on the effects of language standardization Explanation on how to reduce time to market by decreasing design cycle time through modeling and simulation Contains more than 25 building block examples that can be used to construct mixed signal IC system models Analysis of 4 different IC systems using various levels of model detail This book is intended for the practicing engineer who would like to gain practical knowledge in

applications of analog behavioral modelling for IC system design *System-on-Chip Methodologies & Design Languages* Peter J. Ashenden, Jean Mermet, Ralf Seepold, 2013-03-14 System on Chip Methodologies the Forum on Design Languages FDL held in Europe and the Asia Pacific Chip Design Language APChDL Conference The papers cover a range of topics including design methods specification and modeling languages tool issues formal verification simulation and synthesis The results presented in these papers will help researchers and practicing engineers keep abreast of developments in this rapidly evolving field **Design Methodologies for Secure Embedded Systems** Alexander Biedermann, Gregor H Molter, 2010-11-29 Embedded systems have been almost invisibly pervading our daily lives for several decades They facilitate smooth operations in avionics automotive electronics or telecommunication New problems arise by the increasing employment interconnection and communication of embedded systems in heterogeneous environments How secure are these embedded systems against attacks or breakdowns Therefore how can embedded systems be designed to be more secure How can embedded systems autonomically react to threats Facing these questions Sorin A Huss is significantly involved in the exploration of design methodologies for secure embedded systems This Festschrift is dedicated to him and his research on the occasion of his 60th birthday Process Variations and Probabilistic Integrated Circuit Design Manfred Dietrich, Joachim Haase, 2011-11-20 Uncertainty in key parameters within a chip and between different chips in the deep sub micron area plays a more and more important role As a result manufacturing process spreads need to be considered during the design process Quantitative methodology is needed to ensure faultless functionality despite existing process variations within given bounds during product development This book presents the technological physical and mathematical fundamentals for a design paradigm shift from a deterministic process to a probability orientated design process for microelectronic circuits Readers will learn to evaluate the different sources of variations in the design flow in order to establish different design variants while applying appropriate methods and tools to evaluate and optimize their design *Computer-Aided Design of Analog Integrated Circuits and Systems* Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog

synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck **Embedded Systems Specification and Design Languages** Eugenio

Villar,2008-05-15 This book is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 07 in September 2007 The book represents the state of the art in research and practice and it identifies new research directions It highlights the role of specification and modelling languages and presents practical experiences with specification and modelling languages **Abstract State Machines**

Egon Börger,Robert Stärk,2012-12-06 System-on-a-Chip Verification Prakash Rashinkar,Peter Paterson,Leena Singh,2007-05-08 System On a Chip Verification Methodology and Techniques is the first book to cover verification strategies and methodologies for SOC verification from system level verification to the design sign off The topics covered include Introduction to the SOC design and verification aspects System level verification in brief Block level verification Analog mixed signal simulation Simulation HW SW Co verification Static netlist verification Physical verification and Design sign off in brief All the verification aspects are illustrated with a single reference design for Bluetooth application System On a Chip Verification Methodology and Techniques takes a systematic approach that covers the following aspects of verification strategy in each chapter Explanation of the objective involved in performing verification after a given design step Features of options available When to use a particular option How to select an option and Limitations of the option This exciting new book will be of interest to all designers and test professionals **Electronic Engine Control Technologies** Ronald K

Jurgen,2004-03-13 In this second edition of Electronic Engine Control Technologies the latest advances and technologies of electronic engine control are explored in a collection of 99 technical papers none of which were included in the book s first edition Editor Ronald K Jurgen offers an informative introduction Neural Networks on the Rise clearly explaining the book s overall format and layout The book then closely examines the many areas surrounding electronic engine control technologies including specific engine controls diagnostics engine modeling innovative solid state hardware and software systems communication techniques for engine control neural network applications and the future of electronic engine controls

Analog VHDL Andrzej T. Rosinski,Alain Vachoux,2012-12-06 Analog VHDL brings together in one place important contributions and up to date research results in this fast moving area Analog VHDL serves as an excellent reference providing insight into some of the most challenging research issues in the field Analog Circuit Design Michiel

Steyaert,Arthur H.M. van Roermund,Johan Huijsing,2006-03-14 Analog Circuit Design contains the contribution of 18 tutorials of the 14th workshop on Advances in Analog Circuit Design Each part discusses a specific todote topic on new and valuable design ideas in the area of analog circuit design Each part is presented by six experts in that field and state of the

art information is shared and overviewed This book is number 14 in this successful series of Analog Circuit Design providing valuable information and excellent overviews of analog circuit design CAD and RF systems Analog Circuit Design is an essential reference source for analog circuit designers and researchers wishing to keep abreast with the latest development in the field The tutorial coverage also makes it suitable for use in an advanced design course System Specification & Design Languages Eugenio Villar, Jean Mermet, 2007-05-08 In this fourth book in the CHDL Series a selection of the best papers presented in FDL 02 is published System Specification and Design Languages contains outstanding research contributions in the four areas mentioned above So The Analog and Mixed Signal system design contributions cover the new methodological approaches like AMS behavioral specification mixed signal modeling and simulation AMS reuse and MEMs design using the new modeling languages such as VHDL AMS Verilog AMS Modelica and analog mixed signal extensions to SystemC UML is the de facto standard for SW development covering the early development stages of requirement analysis and system specification The UML based system specification and design contributions address latest results on hot topic areas such as system profiling performance analysis and UML application to complex HW SW embedded systems and SoC design C C for HW SW systems design is entering standard industrial design flows Selected papers cover system modeling system verification and SW generation The papers from the Specification Formalisms for Proven design workshop present formal methods for system modeling and design semantic integrity and formal languages such as ALPHA HANDLE and B

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