



Synplify ProTM

Synplify Pro Reference Manual

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David Harris, Sarah Harris



Synplify Pro Reference Manual:

Guide to FPGA Implementation of Arithmetic Functions Jean-Pierre Deschamps, Gustavo D. Sutter, Enrique Cantó, 2012-04-02 This book is designed both for FPGA users interested in developing new specific components generally for reducing execution times and IP core designers interested in extending their catalog of specific components The main focus is circuit synthesis and the discussion shows for example how a given algorithm executing some complex function can be translated to a synthesizable circuit description as well as which are the best choices the designer can make to reduce the circuit cost latency or power consumption This is not a book on algorithms It is a book that shows how to translate efficiently an algorithm to a circuit using techniques such as parallelism pipeline loop unrolling and others Numerous examples of FPGA implementation are described throughout this book and the circuits are modeled in VHDL Complete and synthesizable source files are available for download

FPGAs and Parallel Architectures for Aerospace Applications Fernanda Kastensmidt, Paolo Rech, 2015-12-07 This book introduces the concepts of soft errors in FPGAs as well as the motivation for using commercial off the shelf COTS FPGAs in mission critical and remote applications such as aerospace The authors describe the effects of radiation in FPGAs present a large set of soft error mitigation techniques that can be applied in these circuits as well as methods for qualifying these circuits under radiation Coverage includes radiation effects in FPGAs fault tolerant techniques for FPGAs use of COTS FPGAs in aerospace applications experimental data of FPGAs under radiation FPGA embedded processors under radiation and fault injection in FPGAs Since dedicated parallel processing architectures such as GPUs have become more desirable in aerospace applications due to high computational power GPU analysis under radiation is also discussed

Embedded and Ubiquitous Computing Edwin Sha, 2006-07-18 This book constitutes the refereed proceedings of the International Conference on Embedded and Ubiquitous Computing EUC 2006 held in Seoul Korea August 2006 The book presents 113 revised full papers together with 3 keynote articles organized in topical sections on power aware computing security and fault tolerance agent and distributed computing wireless communications real time systems embedded systems multimedia and data management mobile computing network protocols middleware and P2P and more

Using PSL/Sugar for Formal and Dynamic Verification Ben Cohen, Srinivasan Venkataramanan, Ajeetha Kumari, 2004

Domain-Specific Processors Shuvra S. Bhattacharyya, Ed F. Deprettere, Jörgen Teich, 2003-11-11 Ranging from low level application and architecture optimizations to high level modeling and exploration concerns this authoritative reference compiles essential research on various levels of abstraction appearing in embedded systems and software design It promotes platform based design for improved system implementation and modeling and enhanced

Synthesizable VHDL Design for FPGAs Eduardo Augusto Bezerra, Djones Vinicius Lettnin, 2013-10-21 The methodology described in this book is the result of many years of research experience in the field of synthesizable VHDL design targeting FPGA based platforms VHDL was first conceived as a documentation language for ASIC designs Afterwards the language was used for the behavioral simulation of

ASICs and also as a design input for synthesis tools VHDL is a rich language but just a small subset of it can be used to write synthesizable code from which a physical circuit can be obtained Usually VHDL books describe both synthesis and simulation aspects of the language but in this book the reader is conducted just through the features acceptable by synthesis tools The book introduces the subjects in a gradual and concise way providing just enough information for the reader to develop their synthesizable digital systems in VHDL The examples in the book were planned targeting an FPGA platform widely used around the world

Digital Design (VHDL) Peter J. Ashenden, 2007-10-24 Digital Design An Embedded Systems Approach Using VHDL provides a foundation in digital design for students in computer engineering electrical engineering and computer science courses It takes an up to date and modern approach of presenting digital logic design as an activity in a larger systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized VHDL examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of VHDL examples to demonstrate HDL hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments Includes worked examples throughout to enhance the reader's understanding and retention of the material Companion Web site includes links to tools for FPGA design from Synplicity Mentor Graphics and Xilinx VHDL source code for all the examples in the book lecture slides laboratory projects and solutions to exercises

FPGA-based Implementation of Signal Processing Systems Roger Woods, John McAllister, Gaye Lightbody, Ying Yi, 2008-10-13 Field programmable gate arrays FPGAs are an increasingly popular technology for implementing digital signal processing DSP systems By allowing designers to create circuit architectures developed for the specific applications high levels of performance can be achieved for many DSP applications providing considerable improvements over conventional microprocessor and dedicated DSP processor solutions The book addresses the key issue in this process specifically the methods and tools needed for the design optimization and implementation of DSP systems in programmable FPGA hardware It presents a review of the leading edge techniques in this field analyzing advanced DSP based design flows for both signal flow graph SFG based and dataflow based implementation system on chip SoC aspects and future trends and challenges for FPGAs The automation of the techniques for component architectural synthesis computational models and the reduction of energy consumption to help improve FPGA performance are given in detail Written from a system level design perspective and with a DSP focus the authors present many practical application examples of complex DSP implementation involving high performance computing e.g. matrix operations such as matrix

multiplication high speed filtering including finite impulse response FIR filters and wave digital filters WDFs adaptive filtering e g recursive least squares RLS filtering transforms such as the fast Fourier transform FFT FPGA based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio telecommunication information audio visual and security applications Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing shall also find this volume of interest *Digital VLSI Systems Design* Seetharaman Ramachandran, 2007-06-14 This book deals with actual design applications rather than the technology of VLSI Systems This book is written basically for an advanced level course in Digital VLSI Systems Design using a Hardware Design Language HDL V ilog This book may be used for teaching undergraduates graduates and research scholars of Electrical Electronics Computer Science and Engineering Embedded Systems Measurements and Instrumentation Applied Electronics and interdis plinary departments such as Biomedical Mechanical Engineering Information Technology Physics etc This book also serves as a reference design manual for practicing engineers and researchers Although this book is written for an vanced level course diligent freelance readers and consultants especially those who do not have a first level exposure of digital logic design may also start using this book after a short term course or self study on digital logic design In order to help these readers as well as regular students the book starts with a good review of digital systems design which lays a solid foundation to understand the rest of this book right up to involved Project Designs unfolded gradually Contents of the Book The book presents new source material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards enabling the serious readers to design VLSI Systems on their own Real Chip Design and Verification Using Verilog and VHDL Ben Cohen, 2002 This book concentrates on common classes of hardware architectures and design problems and focuses on the process of transitioning design requirements into synthesizable HDL code Using his extensive wide ranging experience in computer architecture and hardware design as well as in his training and consulting work Ben provides numerous examples of real life designs illustrated with VHDL and Verilog code This code is shown in a way that makes it easy for the reader to gain a greater understanding of the languages and how they compare All code presented in the book is included on the companion CD along with other information such as application notes *Digital Design and Computer Architecture* David Harris, Sarah Harris, 2010-07-26 Digital Design and Computer Architecture is designed for courses that combine digital logic design with computer organization architecture or that teach these subjects as a two course sequence Digital Design and Computer Architecture begins with a modern approach by rigorously covering the fundamentals of digital logic design and then introducing Hardware Description Languages HDLs Featuring examples of the two most widely used HDLs VHDL and Verilog the first half of the text prepares the reader for what follows in the second the design of a MIPS Processor By the end of Digital Design and Computer Architecture readers will be able to build their own microprocessor and

will have a top to bottom understanding of how it works even if they have no formal background in design or architecture beyond an introductory class David Harris and Sarah Harris combine an engaging and humorous writing style with an updated and hands on approach to digital design Unique presentation of digital logic design from the perspective of computer architecture using a real instruction set MIPS Side by side examples of the two most prominent Hardware Design Languages VHDL and Verilog illustrate and compare the ways the each can be used in the design of digital systems Worked examples conclude each section to enhance the reader s understanding and retention of the material Digital Design (Verilog) Peter J. Ashenden,2007-10-24 Digital Design An Embedded Systems Approach Using Verilog provides a foundation in digital design for students in computer engineering electrical engineering and computer science courses It takes an up to date and modern approach of presenting digital logic design as an activity in a larger systems design context Rather than focus on aspects of digital design that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills Hardware description language HDL based design and verification is emphasized Verilog examples are used extensively throughout By treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components Includes a Web site with links to vendor tools labs and tutorials Presents digital logic design as an activity in a larger systems design context Features extensive use of Verilog examples to demonstrate HDL hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments Includes worked examples throughout to enhance the reader s understanding and retention of the material Companion Web site includes links to tools for FPGA design from Synplicity Mentor Graphics and Xilinx Verilog source code for all the examples in the book lecture slides laboratory projects and solutions to exercises □□□□ ,2002

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