

## VLSI Objective Questions and answers

- 1) Chip utilization depends on \_\_\_\_.
  - a. Only on standard cells
  - b. Standard cells and macros
  - c. Only on macros
  - d. Standard cells macros and IO pads
- 2) In Soft blockages \_\_\_\_ cells are placed.
  - a. Only sequential cells
  - b. No cells
  - c. Only Buffers and Inverters
  - d. Any cells
- 3) Why we have to remove scan chains before placement ?
  - a. Because scan chains are group of flip flop
  - b. It does not have timing critical path
  - c. It is series of flip flop connected in FIFO
  - d. None
- 4) Delay between shortest path and longest path in the clock is called \_\_\_\_.
  - a. Useful skew
  - b. Local skew
  - c. Global skew
  - d. Slack
- 5) Cross talk can be avoided by \_\_\_\_
  - a. Decreasing the spacing between the metal layers
  - b. Shielding the nets
  - c. Using lower metal layers
  - d. Using long nets
- 6) Prerouting means routing of \_\_\_\_\_.
  - a. Clock nets
  - b. Signal nets
  - c. IO nets
  - d. PG nets
- 7) Which of the following metal layer has Maximum resistance?
  - a. Metal1
  - b. Metal2
  - c. Metal3
  - d. Metal4
- 8) What is the goal of CTS ?

# Vlsi Objective Questions With Answers

**JS Bruner**



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