

**Class: III SEM CSE**  
**Hours/ week: 3**  
**IA Marks: 25**

**Total Hours: 42**  
**Exam Marks: 50**  
**Exam hours: 3**

## **PART A**

- 1 a. To study the working of Positive Clipper, Double – ended Clipper and Positive Clamper using Diodes  
b. To build and simulate the above circuits using a simulation package.
- 2 a. To determine the Frequency Response, Input Impedance, Output Impedance and Bandwidth of a CE Amplifier.  
b. To build the CE Amplifier using a simulation package and determine the Voltage Gain for two different values of supply voltage and for two different values of Emitter resistance.
- 3 a. To determine drain characteristics and transconductance characteristics of an enhancement mode MOSFET.  
b. To implement a CMOS inverter using a simulation package and verify its Truth Table.
- 4 a. To design and implement a Schmitt Trigger using Op-Amp for a given UTP and LTP values  
b. To implement a Schmitt Trigger using OP – Amp using a simulation package for two sets of UTP and LTP values.
- 5 a. To design and implement a Rectangular Waveform Generator (OP – Amp Relaxation Oscillator) for given frequency.  
b. To implement a Rectangular Waveform Generator (OP – Amp Relaxation Oscillator) using a simulation and observe the change in frequency when all resistor values are doubled.
6. To design and implement an Astable Multivibrator circuit using 555 timer for a given frequency and duty cycle.
7. To implement a +5v regulated power supply using full – wave rectifier and 7805 IC regulator in simulation package. Find the output ripple for different values of load current.

# Vtu Analog Electronics Lab Manual

**Chao Zhang**



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