

## EXPERIEMENT NO. 2

### Simulation using all the modeling styles and Synthesis of 1-bit half adder and 1-bit Full adder using verilog HDL

#### AIM:

Perform Zero Delay Simulation of 1-bit half adder and 1-bit Full adder written in behavioral, dataflow and structural modeling style in VERILOG HDL using a Test bench. Then, Synthesize each one of them on two different EDA tools.

#### Electronics Design Automation Tools used:

- i) FPGA Advantage 3.1 (includes Model Sim simulation tool and Leonardo Spectrum Synthesis Tool)
- ii) Xilinx Project Navigator 8.1 (Includes all the steps in the design flow from Simulation to Implementation to download onto FPGA).

#### Block Diagram:

1-bit Half Adder:



1-bit Full Adder:



#### Truth table:

Half Adder:

A	B	Sum	Carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

Full Adder:

A	B	Cin	Sum	Cout
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# Vhdl Lab Manual Question And Ans

**D. Naylor,S. Jones**



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**VHDL Answers to Frequently Asked Questions** Ben Cohen, 2013-03-09 VHDL Answers to Frequently asked Questions is a follow up to the author's book VHDL Coding Styles and Methodologies ISBN 0 7923 9598 0 On completion of his first book the author continued teaching VHDL and actively participated in the comp lang vhdl newsgroup During his experiences he was enlightened by the many interesting issues and questions relating to VHDL and synthesis These pertained to misinterpretations in the use of the language methods for writing error free and simulation efficient code for testbench designs and for synthesis and general principles and guidelines for design verification As a result of this wealth of public knowledge contributed by a large VHDL community the author decided to act as a facilitator of this information by collecting different classes of VHDL issues and by elaborating on these topics through complete simulatable examples This book is intended for those who are seeking an enhanced proficiency in VHDL Its target audience includes 1 Engineers The book addresses a set of problems commonly experienced by real users of VHDL It provides practical explanations to the questions and suggests practical solutions to the raised issues It also includes packages to achieve common utilities useful in the generation of debug code and testbench designs These packages include conversions to strings the IMAGE package generation of Linear Feedback Shift Registers LFSR Multiple Input Shift Register MISR and random number generators

Digital Circuit Design Laboratory Manual, 4th edition (Global) Akhan Almagambetov, J. Matt Pavlina, Yelena Mukhortova,

*VHDL: A Logic Synthesis Approach* D. Naylor, S. Jones, 1997-07-31 This book is structured in a practical example driven manner The use of VHDL for constructing logic synthesizers is one of the aims of the book the second is the application of the tools to the design process Worked examples questions and answers are provided together with do and don'ts of good practice An appendix on logic design the source code are available free of charge over the Internet **Digital Design with**

**CPLD Applications and VHDL** Robert K. Dueck, 2001 A guide that uses programmable logic as the vehicle for instructing readers in the principles of digital design Following discussion of digital fundamentals the book introduces readers to Complex Programmable Logic Devices Graphic design files VHDL files and simulation files are on the CD ROM so readers can run simulations or program CPLDs with error free design files and use these files as templates for their own modifications **Innovations in E-learning, Instruction Technology, Assessment and Engineering Education**

Magued Iskander, 2007-09-04 This book includes a set of rigorously reviewed world class manuscripts addressing and detailing state of the art research projects in the areas of Engineering Education Instructional Technology Assessment and E learning The book presents selected papers from the conference proceedings of the International Conference on Engineering Education Instructional Technology Assessment and E learning EIAE 2006 All aspects of the conference were managed on line

**Subject Guide to Books in Print**, 2001 Publications of the National Institute of Standards and Technology ...  
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*Proceedings Annual Reliability and Maintainability Symposium* ,1987      *Introduction to Digital Systems* Milos D. Ercegovic, Tom's Lang, Jaime H. Moreno, 1998-10-29 This manual covers more topics related to the field advanced implementation of algorithmic systems and advanced material on VHDL design A real emphasis is placed on the hierarchical approach to the design of digital systems and is followed consistently throughout the text      **Complete Digital Design : A Comprehensive Guide to Digital Electronics and Computer System Architecture** Mark Balch, 2003-06-20 This is a readable hands on self tutorial through basic digital electronic design methods The format and content allows readers faced with a design problem to understand its unique requirements and then research and evaluate the components and technologies required to solve it Begins with basic design elements and expands into full systems Covers digital analog and full system designs Features real world implementation of complete digital systems      **Scientific and Technical Aerospace Reports** ,1991      **Verilog® Quickstart** James M. Lee, 2006-01-12 From a review of the Second Edition If you are new to the field and want to know what all this Verilog stuff is about you've found the golden goose The text here is straight forward complete and example rich mega multi kudos to the author James Lee Though not as detailed as the Verilog reference guides from Cadence it likewise doesn't suffer from the excessive abstractness those make you wade through This is a quick and easy read and will serve as a desktop reference for as long as Verilog lives Best testimonial I'm buying my fourth and fifth copies tonight I've loaned out lost two of my others Zach Coombes AMD      **Applied Science & Technology Index** ,1996      Electrical & Electronics Abstracts ,1997      **Books In Print 2004-2005** Ed Bowker Staff, Staff Bowker, Ed, 2004      *Subject Guide to Children's Books in Print* 1997 Bowker Editorial Staff, R R Bowker Publishing, 1996-09      **Government Reports Announcements & Index** ,1989-05      **Government Reports Annual Index** ,1990      **Dr. Dobb's Journal** ,1992

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