

VLSI DIGITAL SIGNAL PROCESSING SYSTEMS

Design and Implementation

Keshab K. Parhi

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FPGA-based Implementation of Signal Processing Systems Roger Woods, John McAllister, Gaye Lightbody, Ying Yi, 2008-10-13 Field programmable gate arrays FPGAs are an increasingly popular technology for implementing digital signal processing DSP systems By allowing designers to create circuit architectures developed for the specific applications high levels of performance can be achieved for many DSP applications providing considerable improvements over conventional microprocessor and dedicated DSP processor solutions The book addresses the key issue in this process specifically the methods and tools needed for the design optimization and implementation of DSP systems in programmable FPGA hardware It presents a review of the leading edge techniques in this field analyzing advanced DSP based design flows for both signal flow graph SFG based and dataflow based implementation system on chip SoC aspects and future trends and challenges for FPGAs The automation of the techniques for component architectural synthesis computational models and the reduction of energy consumption to help improve FPGA performance are given in detail Written from a system level design perspective and with a DSP focus the authors present many practical application examples of complex DSP implementation involving high performance computing e g matrix operations such as matrix multiplication high speed filtering including finite impulse response FIR filters and wave digital filters WDFs adaptive filtering e g recursive least squares RLS filtering transforms such as the fast Fourier transform FFT FPGA based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio telecommunication information audio visual and security applications Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing shall also find this volume of interest *Digital Signal Processing* Paulo S. R. Diniz, Eduardo A. B. da Silva, Sergio L. Netto, 2002-04-18 Digital signal processing lies at the heart of the communications revolution and is an essential element of key technologies such as mobile phones and the Internet This book covers all the major topics in digital signal processing DSP design and analysis supported by MatLab examples and other modelling techniques The authors explain clearly and concisely why and how to use digital signal processing systems how to approximate a desired transfer function characteristic using polynomials and ratio of polynomials why an appropriate mapping of a transfer function on to a suitable structure is important for practical applications and how to analyse represent and explore the trade off between time and frequency representation of signals An ideal textbook for students it will also be a useful reference for engineers working on the development of signal processing systems Digital Design of Signal Processing Systems Shoab Ahmed Khan, 2011-07-28 Digital Design of Signal Processing Systems discusses a spectrum of architectures and methods for effective implementation of algorithms in hardware HW Encompassing all facets of the subject this book includes conversion of algorithms from floating point to fixed point format parallel architectures for basic computational blocks Verilog Hardware Description Language HDL SystemVerilog and coding guidelines for synthesis The

book also covers system level design of Multi Processor System on Chip MPSoC a consideration of different design methodologies including Network on Chip NoC and Kahn Process Network KPN based connectivity among processing elements A special emphasis is placed on implementing streaming applications like a digital communication system in HW Several novel architectures for implementing commonly used algorithms in signal processing are also revealed With a comprehensive coverage of topics the book provides an appropriate mix of examples to illustrate the design methodology Key Features A practical guide to designing efficient digital systems covering the complete spectrum of digital design from a digital signal processing perspective Provides a full account of HW building blocks and their architectures while also elaborating effective use of embedded computational resources such as multipliers adders and memories in FPGAs Covers a system level architecture using NoC and KPN for streaming applications giving examples of structuring MATLAB code and its easy mapping in HW for these applications Explains state machine based and Micro Program architectures with comprehensive case studies for mapping complex applications The techniques and examples discussed in this book are used in the award winning products from the Center for Advanced Research in Engineering CARE Software Defined Radio 10 Gigabit VoIP monitoring system and Digital Surveillance equipment has respectively won APICTA Asia Pacific Information and Communication Alliance awards in 2010 for their unique and effective designs

Intelligent Methods in Signal Processing and Communications Domingo DoCampo, Anibal Figueiras-Vidal, Fernando Perez-González, 2012-12-06 129 6 2 Representation of hints 131 6 3 Monotonicity hints 134 6 4 Theory 139 6 4 1 Capacity results 140 6 4 2 Decision boundaries 144 6 5 Conclusion 145 6 6 References 146 7 Analysis and Synthesis Tools for Robust SPRness 147 C Mosquera J R Hernandez F Perez Gonzalez 7 1 Introduction 147 7 2 SPR Analysis of Uncertain Systems 153 7 2 1 The Poly topic Case 155 7 2 2 The ZP Ball Case 157 7 2 3 The Roots Space Case 159 7 3 Synthesis of LTI Filters for Robust SPR Problems 161 7 3 1 Algebraic Design for Two Plants 161 7 3 2 Algebraic Design for Three or More Plants 164 7 3 3 Approximate Design Methods 165 7 4 Experimental results 167 7 5 Conclusions 168 7 6 References 169 8 Boundary Methods for Distribution Analysis 173 J L Sancho et al 8 1 Introduction 173 8 1 1 Building a Classifier System 175 8 2 Motivation 176 8 3 Boundary Methods as Feature Set Evaluation 177 8 3 1 Results 179 8 3 2 Feature Set Evaluation using Boundary Methods S mary 182

VLSI Signal Processing, IX IEEE Signal Processing Society, 1996 VLSI signal processing components of portable systems in communications are described System equipment and component designs for low power and small size critical to product success are described Applications to the wireless network and cellular communications are given

IEEE Circuits & Devices, 2000

Custom Memory Management Methodology Francky Catthoor, Sven Wuytack, G.E. de Greef, Florin Banica, Lode Nachtergaele, Arnout Vandecappelle, 2013-03-09 The main intention of this book is to give an impression of the state of the art in system level memory management data transfer and storage related issues for complex data dominated real time signal and data processing applications The material is based on research at IMEC in this area in the period 1989

1997 In order to deal with the stringent timing requirements and the data dominated characteristics of this domain we have adopted a target architecture style and a systematic methodology to make the exploration and optimization of such systems feasible Our approach is also very heavily application driven which is illustrated by several realistic demonstrators partly used as red thread examples in the book Moreover the book addresses only the steps above the traditional high level synthesis scheduling and allocation or compilation traditional or ILP oriented tasks The latter are mainly focussed on scalar or scalar stream operations and data where the internal structure of the complex data types is not exploited in contrast to the approaches discussed here The proposed methodologies are largely independent of the level of programmability in the data path and controller so they are valuable for the realisation of both hardware and software systems Our target domain consists of signal and data processing systems which deal with large amounts of data

High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Systems Design and Applications K. J. Ray Liu, Kung Yao, 1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 2 Systems Design And Applications The second volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of the latest design tools design environments and implementations of VLSI signal processing systems These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends

VLSI Signal Processing, VIII IEEE Signal Processing Society, 1995 *VLSI Digital Signal Processing Systems* Keshab K. Parhi, 1999 This graduate level textbook brings together the distinct fields of computer architecture theory and digital signal processing Covering a large amount of material in a single self contained volume it links VLSI architecture with the algorithms on which the accompanying hardware is based

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