

Question Paper Code : 71738

B.E./B.Tech. DEGREE EXAMINATION, APRIL/MAY 2017.

Sixth/Seventh/Eighth Semester

Electronics and Communication Engineering

EC 6601 — VLSI DESIGN

(Common to Biomedical Engineering/B.E. Electrical and Electronic Engineering/
B.E. Electronics and Communication Engineering/B.E. Electronics and
Instrumentation Engineering/B.E. Medical Electronics Engineering/
B.E. Robotics and Automation Engineering)

(Regulations 2013)

Time : Three hours

Maximum : 100 marks

Answer ALL questions.

PART A — (10 × 2 = 20 marks)

1. What is meant Channel length modulation in NMOS transistors?
2. Define propagation delay of a CMOS inverter.
3. Define Elmore constant.
4. State the advantages of transmission gates.
5. What is meant by pipelining?
6. Compare and contrast synchronous design and asynchronous design.
7. List out the components of data path.
8. Give the application of high speed adder.
9. What is meant by CBIC?
10. Name the elements in a Configuration Logic Block.

Vlsi Design Ece Question Paper

Josef A. Nossek



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Principles of VLSI and CMOS Integrated Circuits Jain Richa & Rai Amrita, 2016 For B E B Tech students of all Technical Universities Microelectronics VLSI Design is an emerging subject in the field of electronics in recent years It is an introductory source to internal parts of electronics at minute level This book is covering CMOS Design from a digital system level to circuit level and providing a background in CMOS Processing Technology The book includes basic theoretical knowledge as well as good engineering practice This book is recommended for B Tech M Tech and diploma students of all Indian Universities and also useful for competitive examinations *Eleventh International Conference on VLSI Design* VLSI Society of India, IEEE Circuits and Systems Society, 1997 Areas covered in this work include physical design synthesis delay test and timing high level synthesis hardware software co design low power design verification VLSI synthesis testability enhancement asynchronous design diagnosis test and fault modelling and mixed signal design *Proceedings*, 2003

Digest of Technical Papers, 1985 **Schedule of Classes** University of California, San Diego, 1994 *FPGA ...*, 2003

Mathematical Reviews, 2004 *ICASSP 89*, 1989 *International Symposium on System Synthesis*, 2002 *IEEE Transactions on Circuits and Systems*, 2006 **Proceedings of the 1994 International Conference on Parallel Processing, August 15-19, 1994: Architecture** Dharma Prakash Agrawal, K. C. Tai, Jagdish Chandra, 1994

Undergraduate and Graduate Courses and Programs Iowa State University, 2007 *30th Midwest Symposium on Circuits and Systems* Kamal Jabbour, 1988 **SQA Specimen Paper 2013 National 5 Design and Manufacture** Scottish Qualifications Authority, 2013-11-21 Test Test *Electronic Circuits Design Questions and Answers PDF* Arshad Iqbal, The Electronic Circuits Design Quiz Questions and Answers PDF Circuits Design Competitive Exam Questions Chapter 1 11 Practice Tests Class 8 12 Electronics Textbook Questions for Beginners includes revision guide for problem solving with hundreds of solved questions Electronic Circuits Design Questions and Answers PDF book covers basic concepts analytical and practical assessment tests Electronic Circuits Design Quiz PDF book helps to practice test questions from exam prep notes The Electronic Circuits Design Quiz Questions and Answers PDF e Book includes revision guide with verbal quantitative and analytical past papers solved tests Electronic Circuits Design Questions and Answers PDF Free download chapter 1 a book covers solved common questions and answers on chapters Amplifier frequency response bipolar junction transistors BJT amplifiers diode applications field effect transistors FET amplifiers introduction to electronics power amplifiers semiconductors basics special purpose diodes transistor bias circuits tests for college and university revision guide Electronics Interview Questions and Answers PDF Download free eBook s sample covers beginner s solved questions textbook s study notes to practice online tests The Electronic Circuits Design Interview Questions Chapter 1 11 PDF book includes high school question papers to review practice tests for exams Electronic Circuits Design Practice Tests a textbook s revision guide with chapters tests for NEET Jobs Entry Level competitive exam Electronic Circuits Design Questions Bank

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Parallel Processing on VLSI Arrays Josef A. Nossek, 2012-12-06 Guest Editor JOSEF A NOSSEK This is a special issue of the Journal of VLSI Signal Processing comprising eight contributions invited for publication on the basis of novel work presented in a special session on Parallel Processing on VLSI Arrays at the International Symposium on Circuits and Systems ISCAS held in New Orleans in May 1990 Massive parallelism to cope with high speed requirements stemming from real time applications and the restrictions in architectural and circuit design such as regularity and local connectedness brought about by the VLSI technology are the key questions addressed in these eight papers They can be grouped into three subsections elaborating on Simulation of continuous physical systems i e numerically solving partial differential equations Neural architectures for image processing and pattern recognition Systolic architectures for implementing regular and irregular algorithms in VLSI technology The paper by A Fettweis and O Nitsche advocates a signal processing approach for the numerical integration of partial differential equations PD Es It is based on the principles of multidimensional wave digital filters MDWDFs thereby preserving the passivity of energy dissipating physical systems It is particularly suited for systems of PDEs involving time and finite propagation speed The basic ideas are explained using Maxwell's equations as a vehicle for the derivation of a multidimensional equivalent circuit representing the spatially infinitely extended arrangement with only very few circuit elements

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