# Objectives

Upon the completion of this Lab, you should be able to:

- 1. Use L-edit software to lay out basic CMOS digital circuits,
- Use T-spice software to analyze static and dynamic characteristics of CMOS digital circuits.

#### Requirements

You are required to draw the vertical and horizontal layouts for the basic CMOS inverter and compare between the two layouts in terms of the used area, power consumption, DC characteristics, and propagation delays. Both inverters should have the same dimensions.

The following sections provide the detailed procedures to draw the layout of the vertical CMOS inverter using L-edit.

#### Manual Layout

1. Copy the following files into your directory

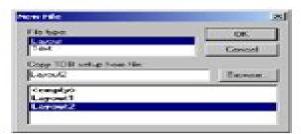
Technology setup files for MOSIS/Orbit n-well 2.0 micron process. (Technology

= SCNA, LAMBDA = 1.0 micron)

Ctanner/ledit83/samples/tech/mosis/morbn20.

- 2. Launch L-Edit
- Create New File. Create new files by choosing File > New, which opens the New

File dialog:



# Vlsi Lab Manual Using Tanner Eda

**Daniel F McAuley** 

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# Table of Contents Vlsi Lab Manual Using Tanner Eda

- 1. Understanding the eBook Vlsi Lab Manual Using Tanner Eda
  - o The Rise of Digital Reading Vlsi Lab Manual Using Tanner Eda
  - Advantages of eBooks Over Traditional Books
- 2. Identifying Vlsi Lab Manual Using Tanner Eda
  - Exploring Different Genres
  - o Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals
- 3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Vlsi Lab Manual Using Tanner Eda
  - User-Friendly Interface
- 4. Exploring eBook Recommendations from Vlsi Lab Manual Using Tanner Eda
  - Personalized Recommendations
  - Vlsi Lab Manual Using Tanner Eda User Reviews and Ratings
  - Vlsi Lab Manual Using Tanner Eda and Bestseller Lists

- 5. Accessing Vlsi Lab Manual Using Tanner Eda Free and Paid eBooks
  - Vlsi Lab Manual Using Tanner Eda Public Domain eBooks
  - Vlsi Lab Manual Using Tanner Eda eBook Subscription Services
  - Vlsi Lab Manual Using Tanner Eda Budget-Friendly Options
- 6. Navigating Vlsi Lab Manual Using Tanner Eda eBook Formats
  - ePub, PDF, MOBI, and More
  - Vlsi Lab Manual Using Tanner Eda Compatibility with Devices
  - Vlsi Lab Manual Using Tanner Eda Enhanced eBook Features
- 7. Enhancing Your Reading Experience
  - o Adjustable Fonts and Text Sizes of Vlsi Lab Manual Using Tanner Eda
  - Highlighting and Note-Taking Vlsi Lab Manual Using Tanner Eda
  - o Interactive Elements Vlsi Lab Manual Using Tanner Eda
- 8. Staying Engaged with Vlsi Lab Manual Using Tanner Eda
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Vlsi Lab Manual Using Tanner Eda
- 9. Balancing eBooks and Physical Books Vlsi Lab Manual Using Tanner Eda
  - Benefits of a Digital Library
  - o Creating a Diverse Reading Collection Vlsi Lab Manual Using Tanner Eda
- 10. Overcoming Reading Challenges
  - $\circ\,$  Dealing with Digital Eye Strain
  - Minimizing Distractions
  - Managing Screen Time
- 11. Cultivating a Reading Routine Vlsi Lab Manual Using Tanner Eda
  - Setting Reading Goals Vlsi Lab Manual Using Tanner Eda
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Vlsi Lab Manual Using Tanner Eda
  - Fact-Checking eBook Content of Vlsi Lab Manual Using Tanner Eda
  - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
- Exploring Educational eBooks
- 14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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